

FIG. 1

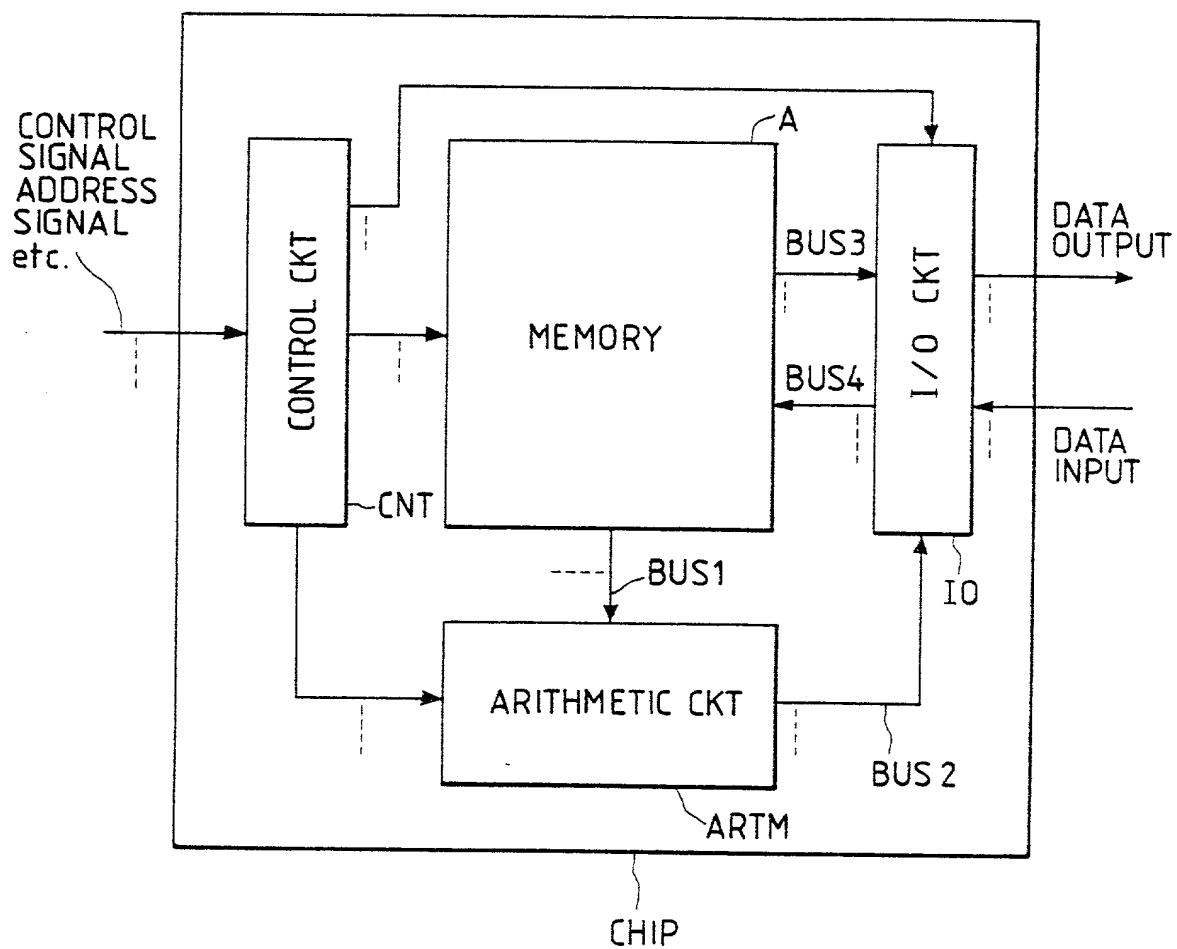


FIG. 2(a)

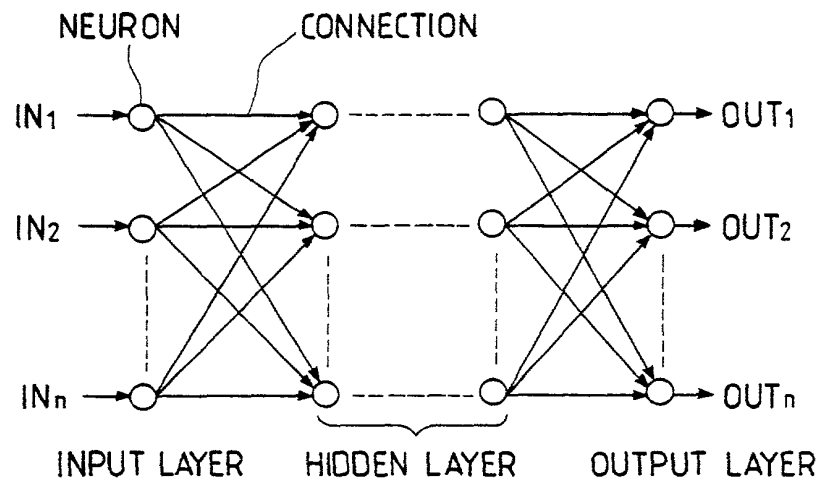


FIG. 2(b)

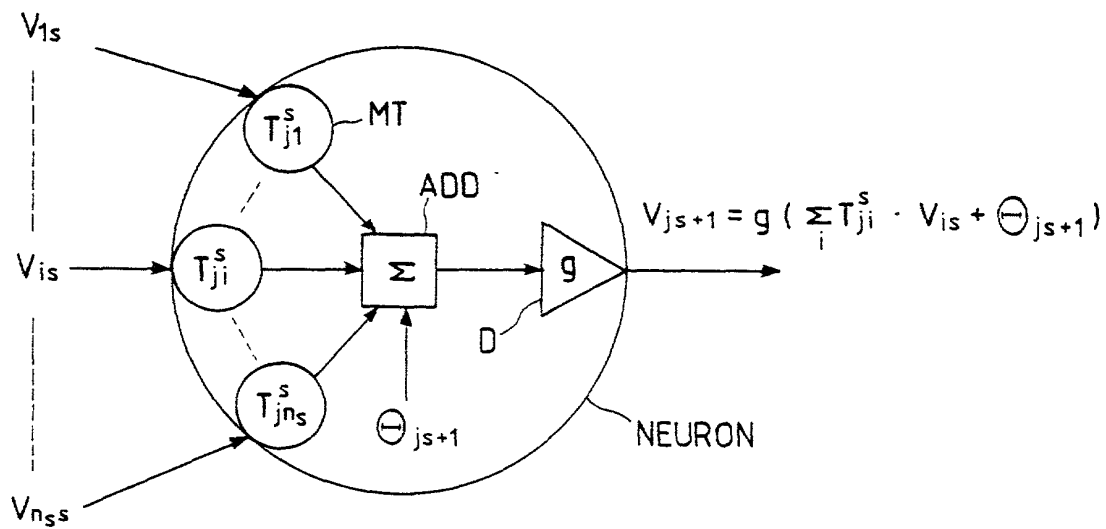


FIG. 2(c)

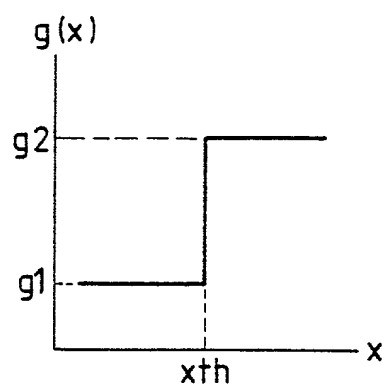


FIG. 2(d)

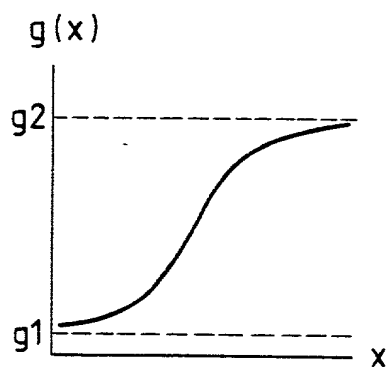


FIG. 3(a)

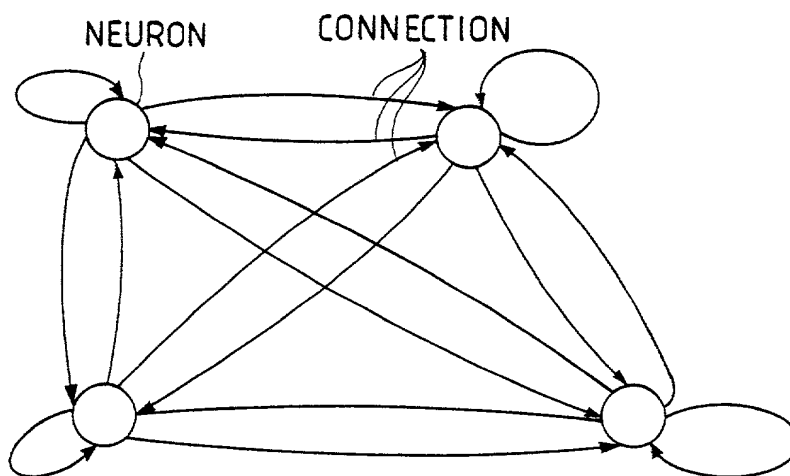


FIG. 3(b)

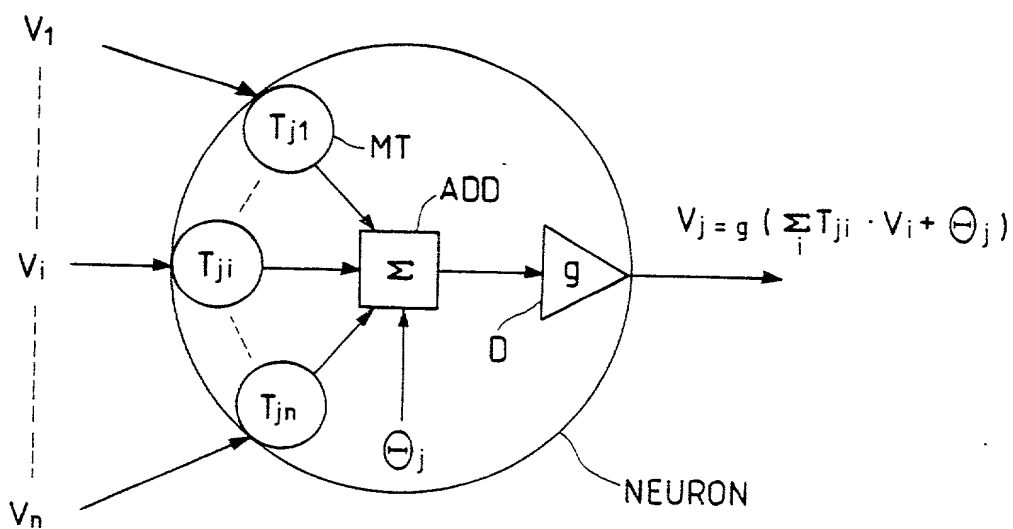


FIG. 4  
(PRIOR ART)

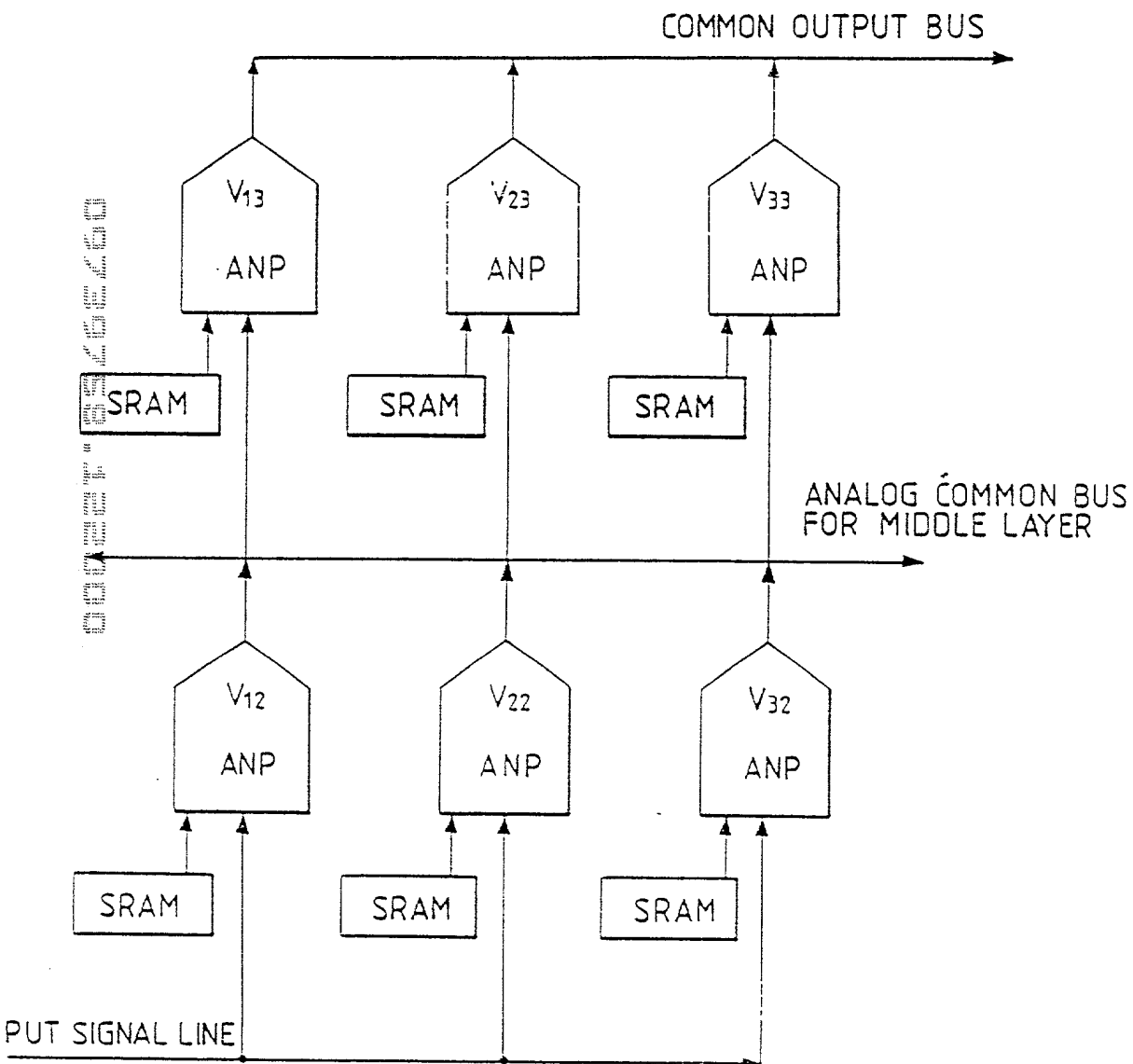


FIG. 5(a)

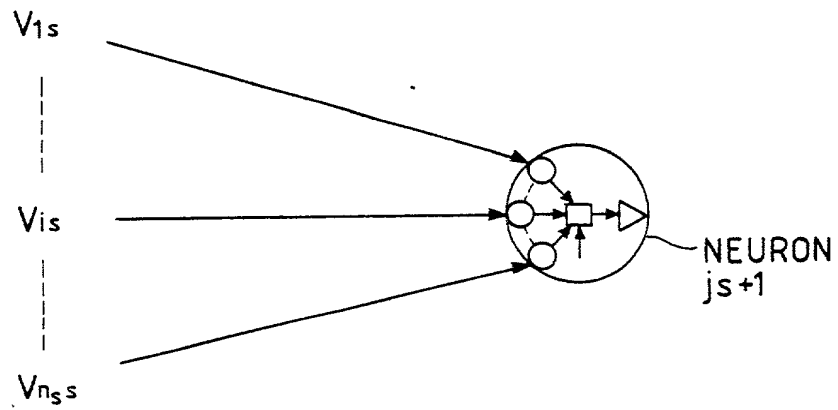


FIG. 5(b)

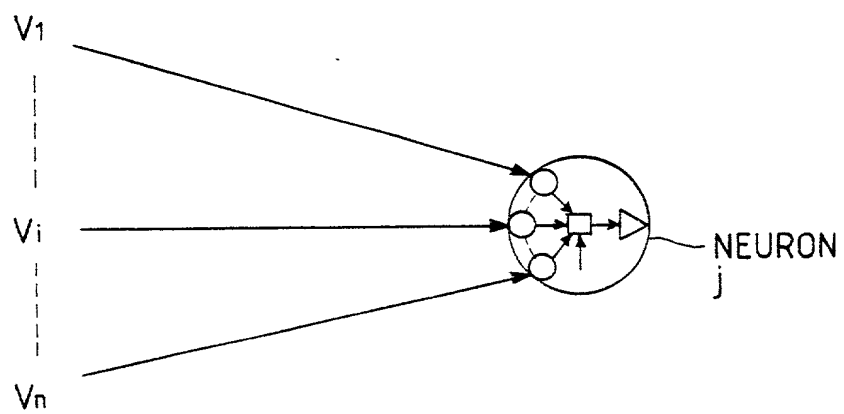


FIG. 6(a)

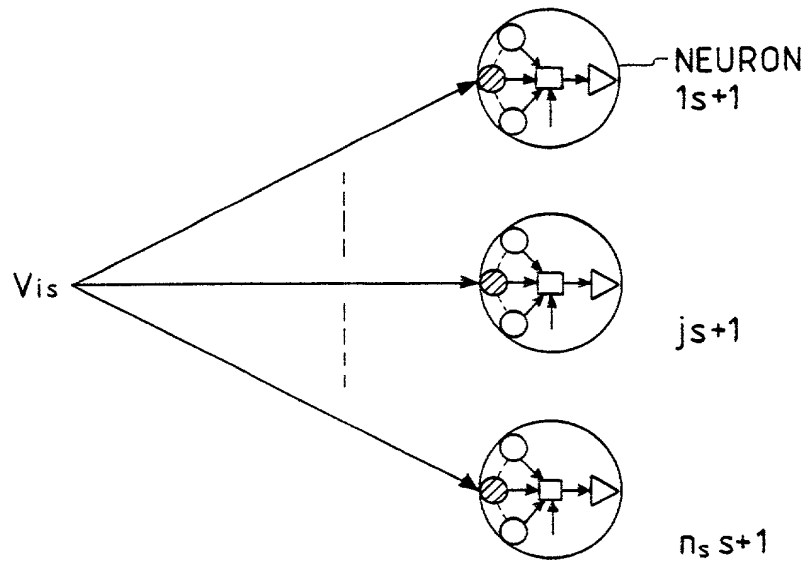


FIG. 6(b)

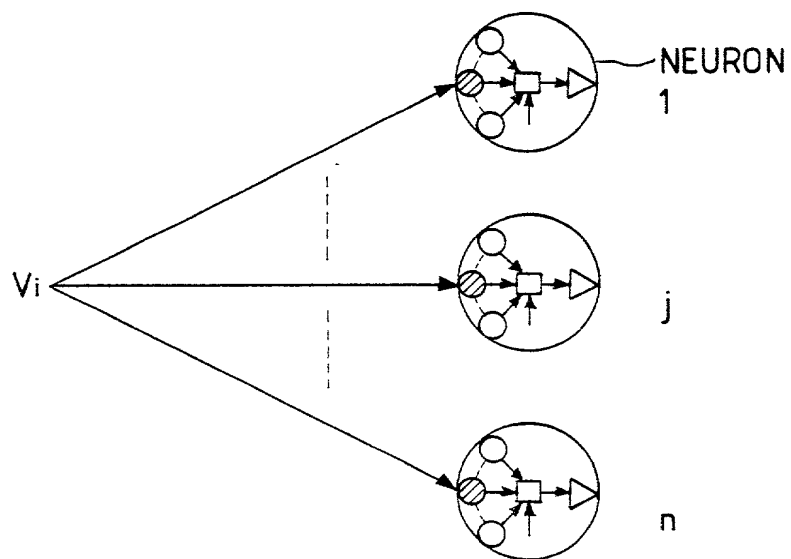


FIG. 7(a)

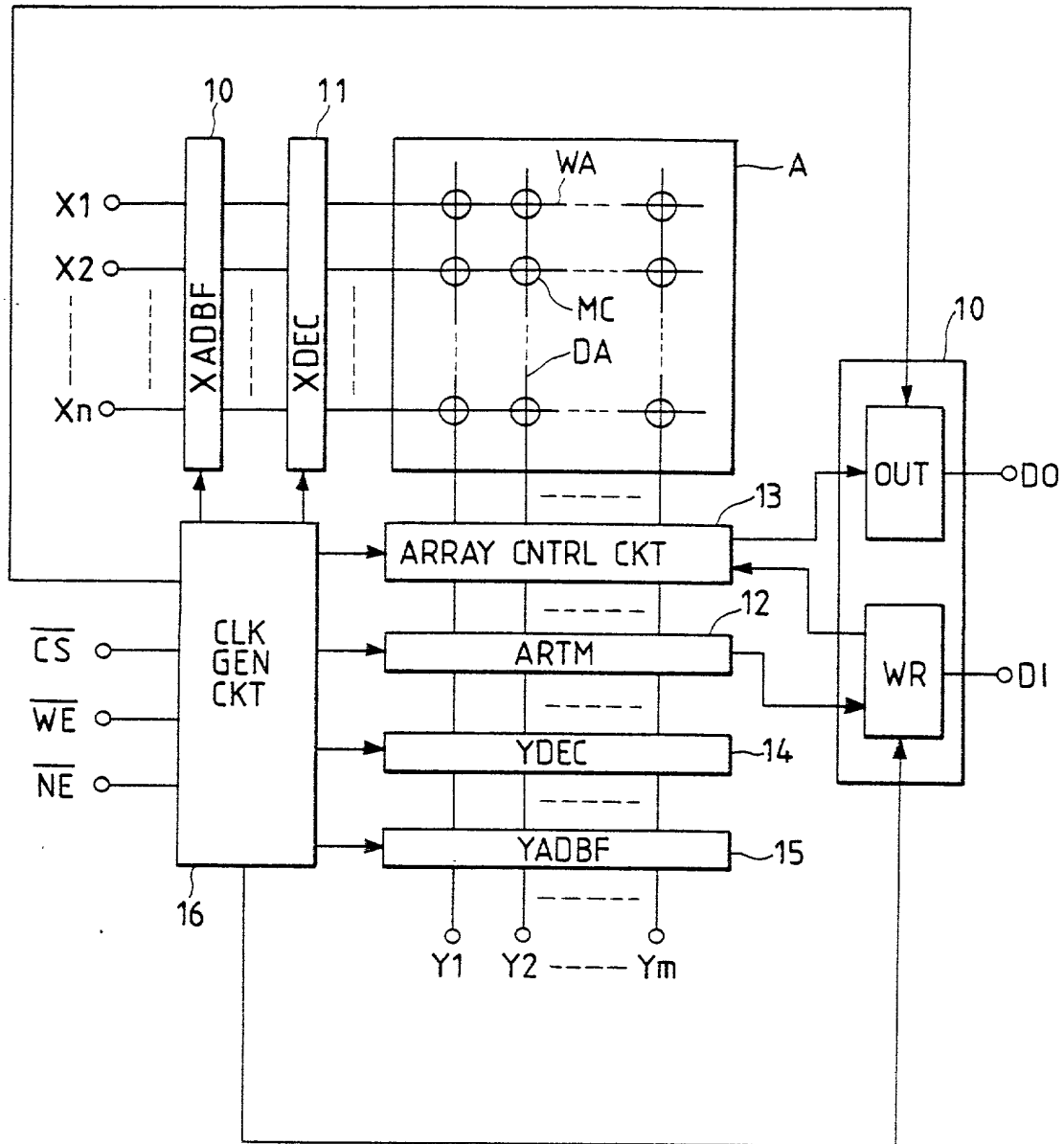




FIG. 7(b)

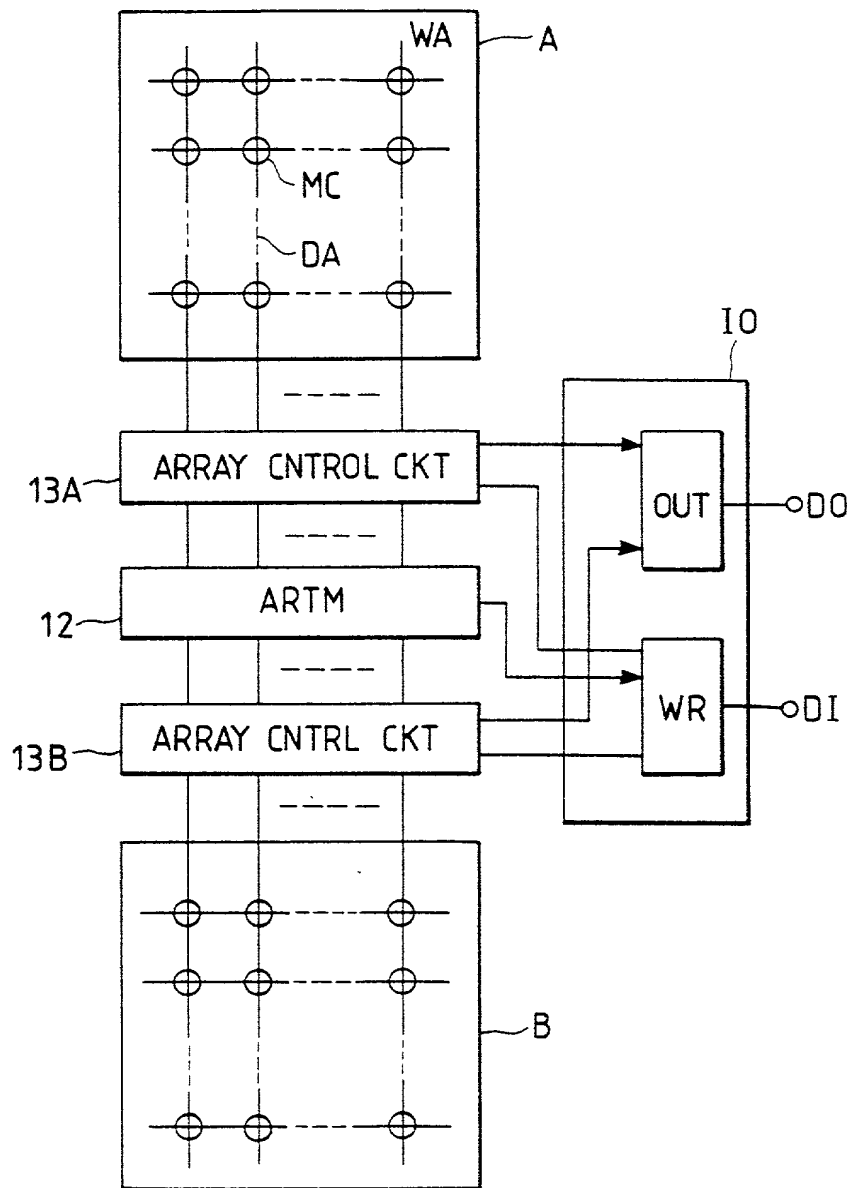


FIG. 8(a)

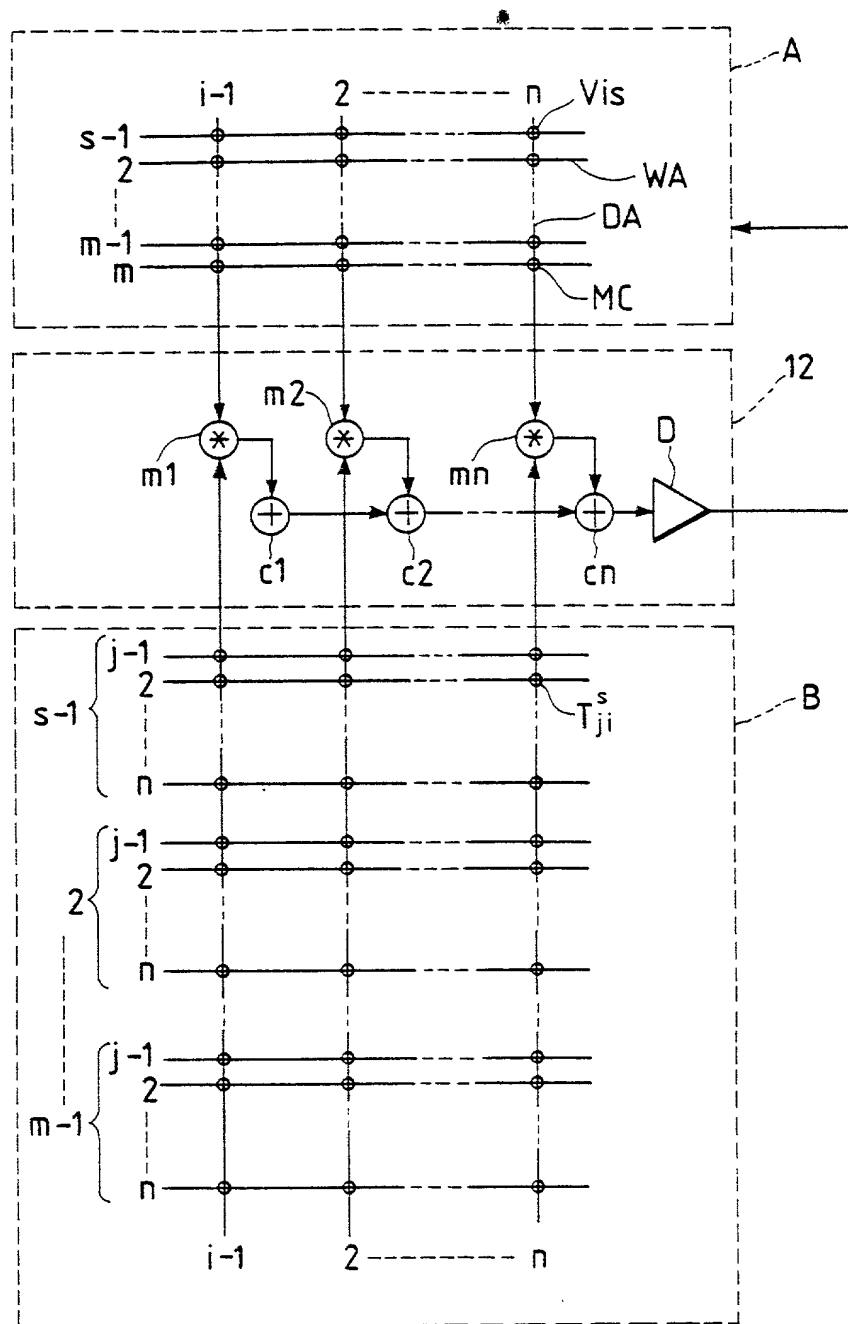


FIG. 8(b)

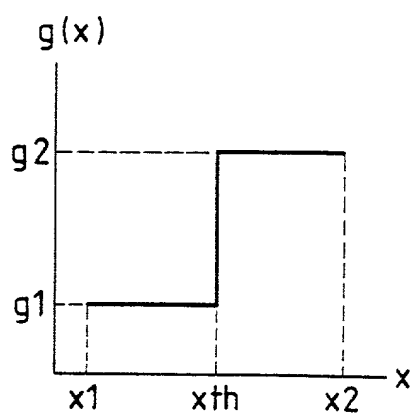


FIG. 8(c)

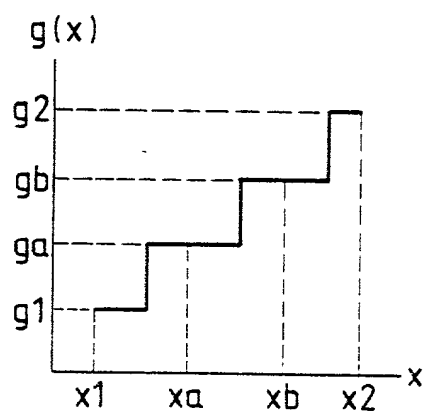


FIG. 8(d)

SELECTED WORD LINE		WRITE ADDRESS
ARRAY A s	ARRAY B (s, j)	ARRAY A (s, i)
1	(1, 1)	(2, 1)
1	(1, 2)	(2, 2)
⋮	⋮	⋮
1	(1, n)	(2, n)
2	(2, 1)	(3, 1)
2	(2, 2)	(3, 2)
⋮	⋮	⋮
2	(2, n)	(3, n)
m - 1	(m - 1, 1)	(m, 1)
m - 1	(m - 1, 2)	(m, 2)
⋮	⋮	⋮
m - 1	(m - 1, m)	(m, n)

FIG. 9(a)

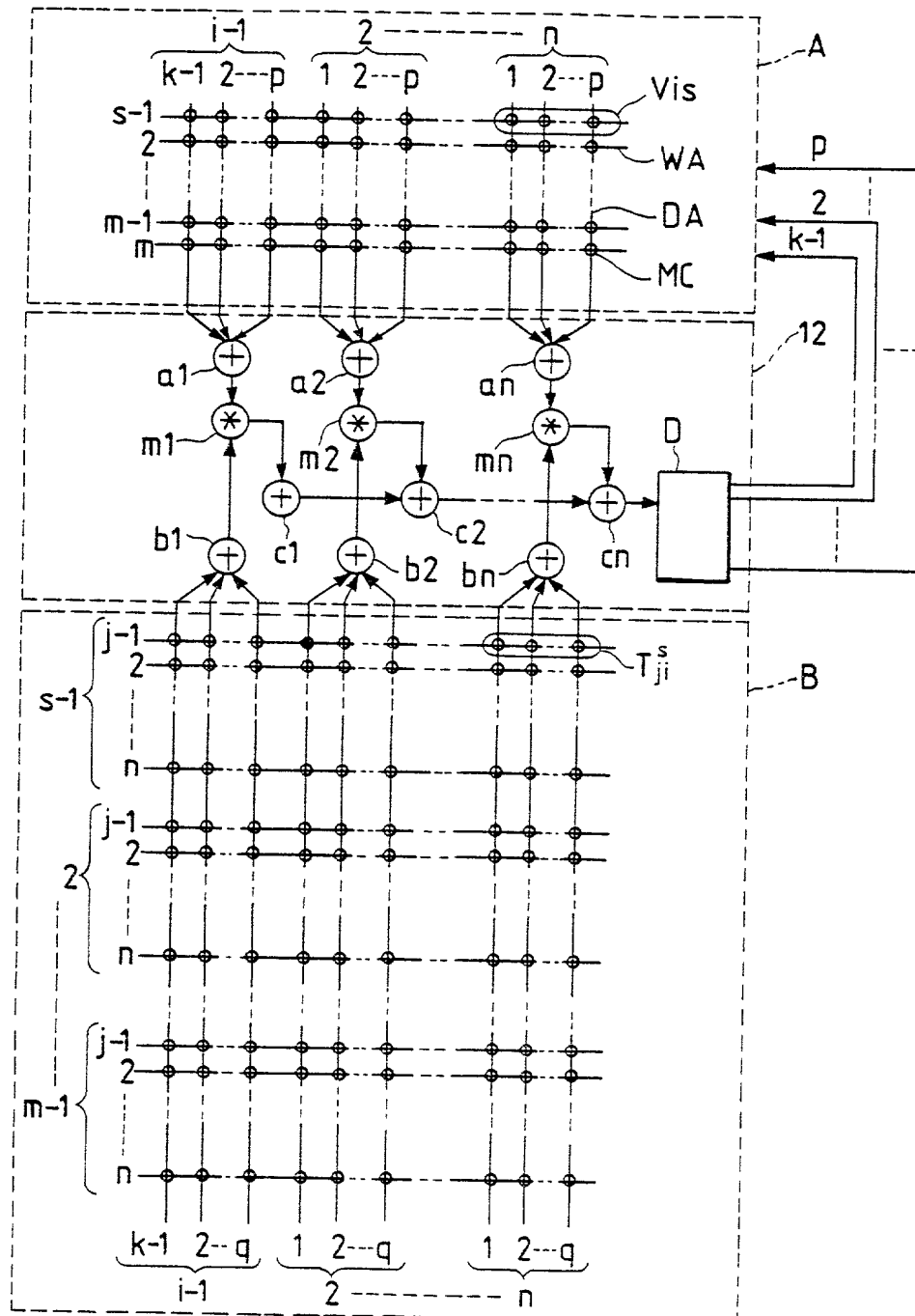


FIG. 9(b)

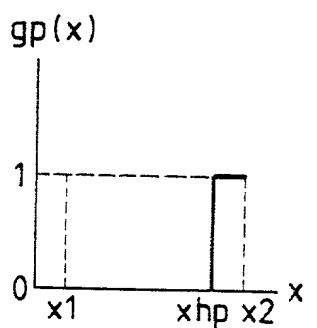
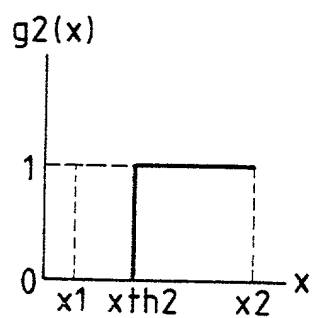
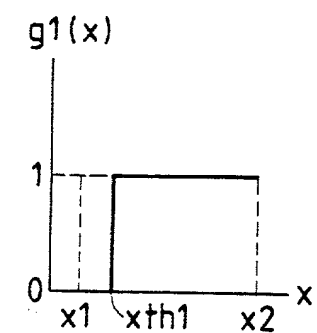


FIG. 9(c)

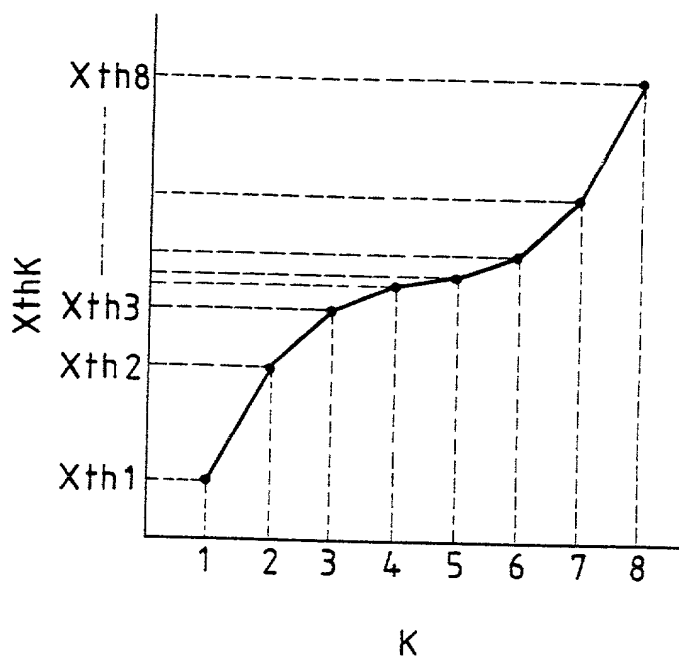


FIG. 10(a)

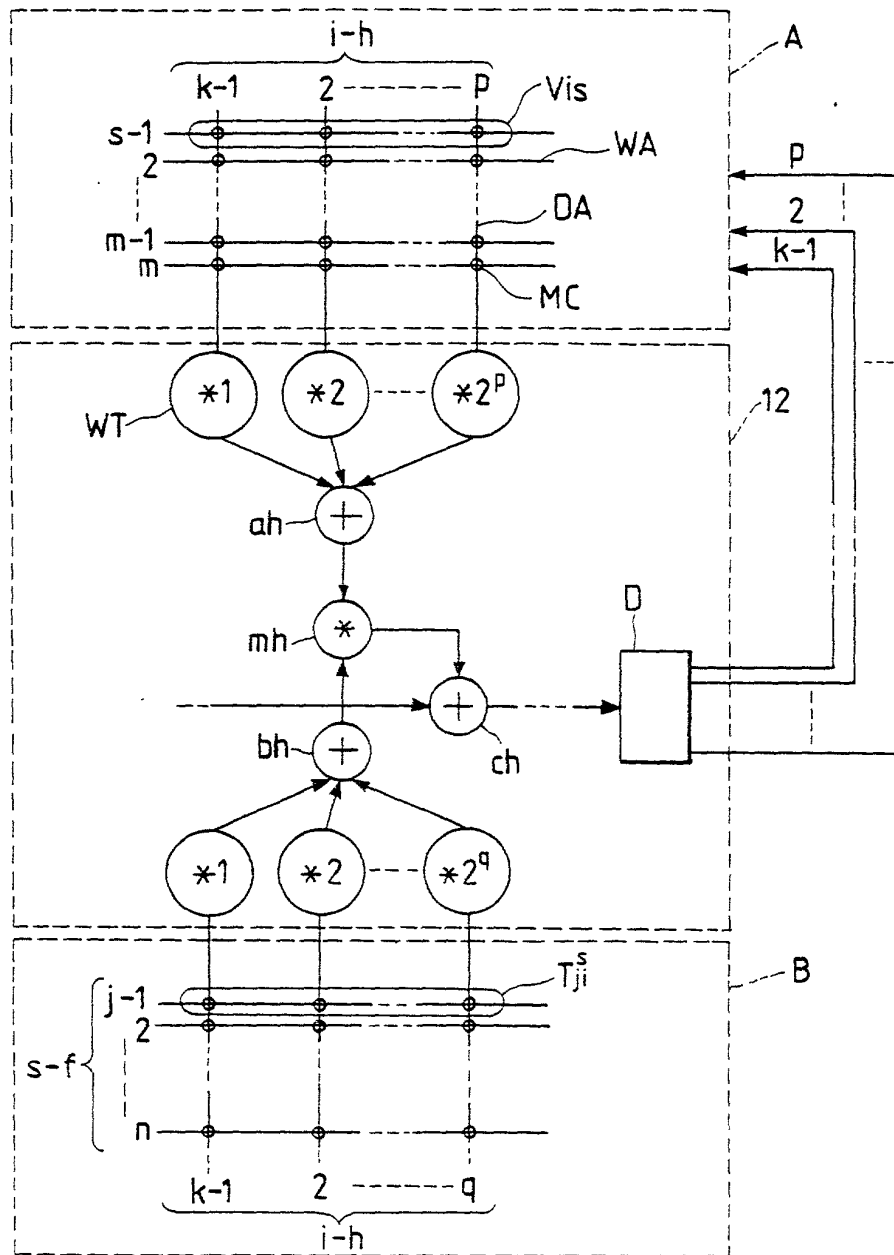


FIG. 10(b)

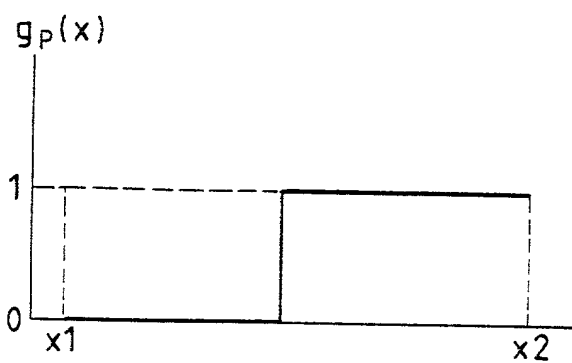
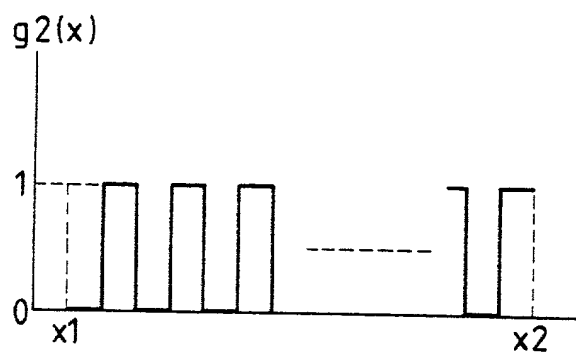
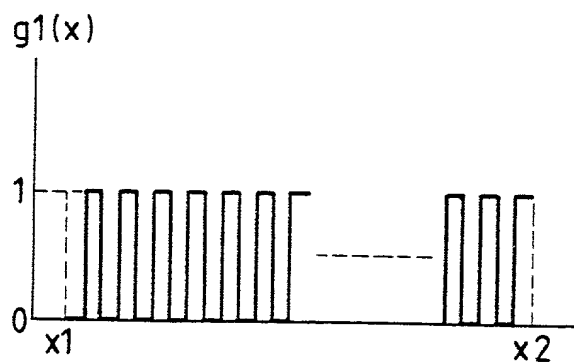




FIG. 11

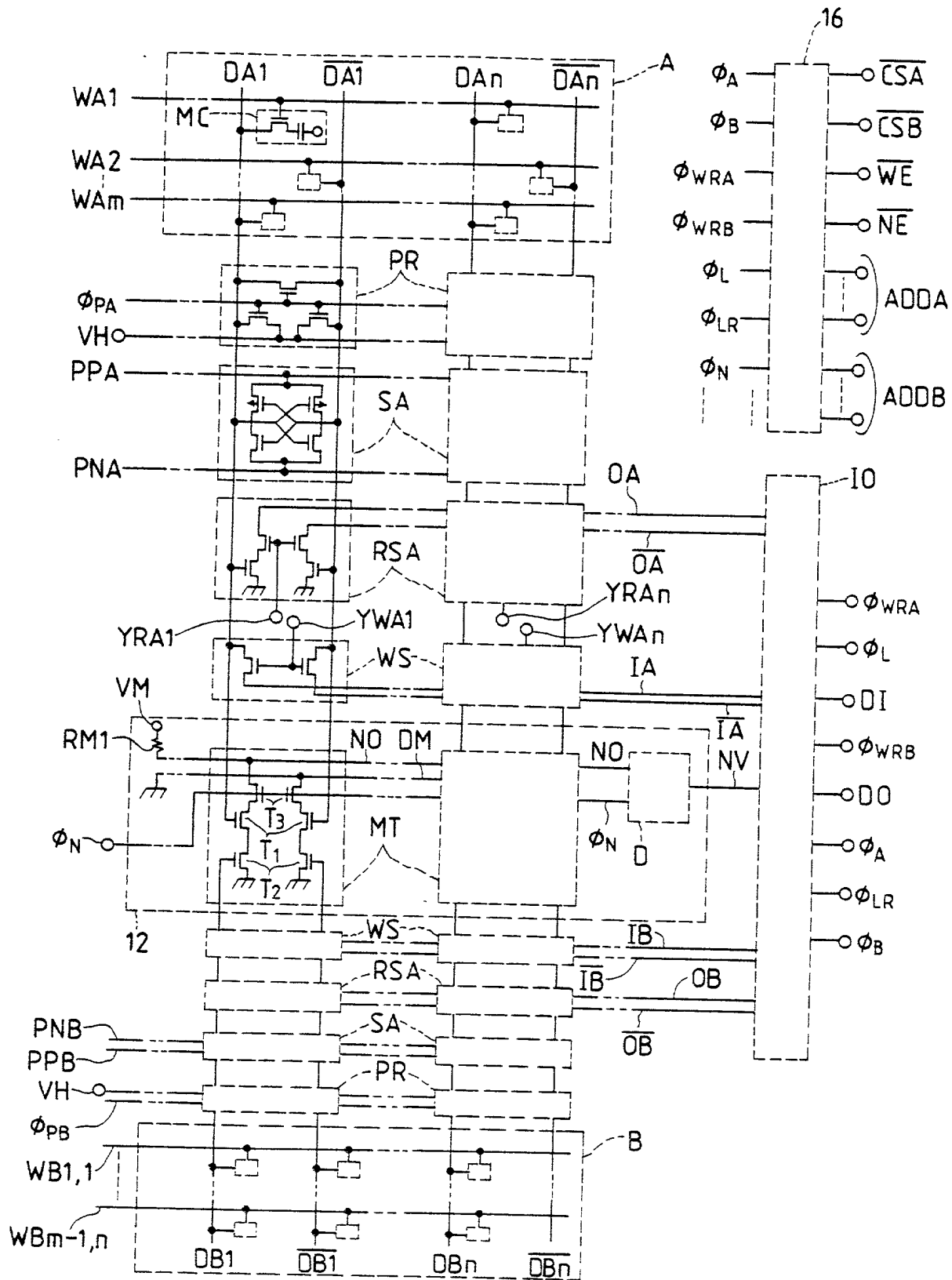


FIG. 12

MODE SIGNAL	MEMORY MODE				ARITHMETIC MODE	
	AR	AW	BR	BW	NR	NW
$\overline{\text{CSA}}$	L	L	H	H	L	L
$\overline{\text{CSB}}$	H	H	L	L	L	H
$\overline{\text{WE}}$	H	L	H	L	H	L
$\overline{\text{NE}}$	H	H	H	H	L	L
ADDRESS	ADDA	ADDA	ADDB	ADDB	ADDA ADDB	ADDA

FIG. 13(a)

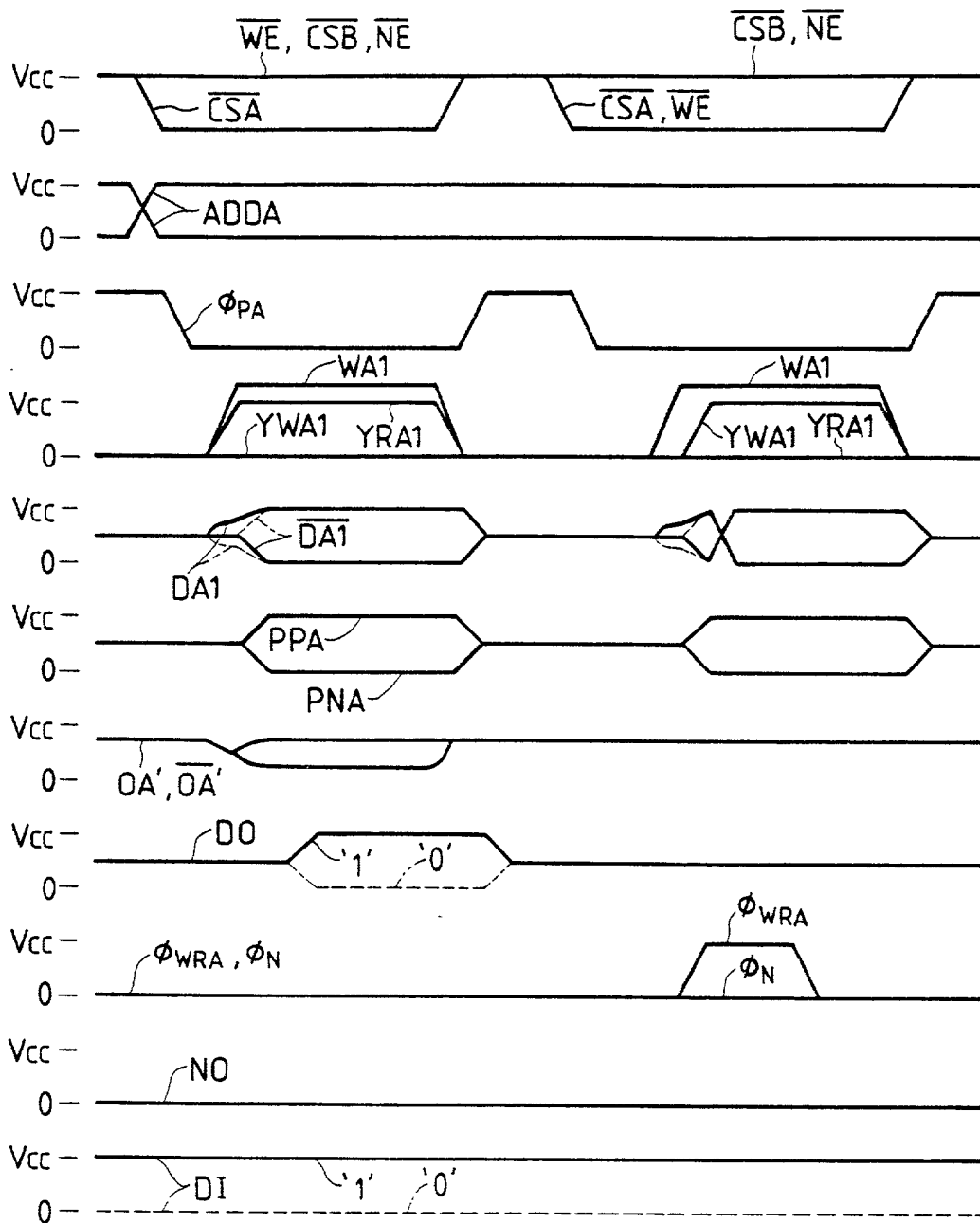


FIG. 13(b)

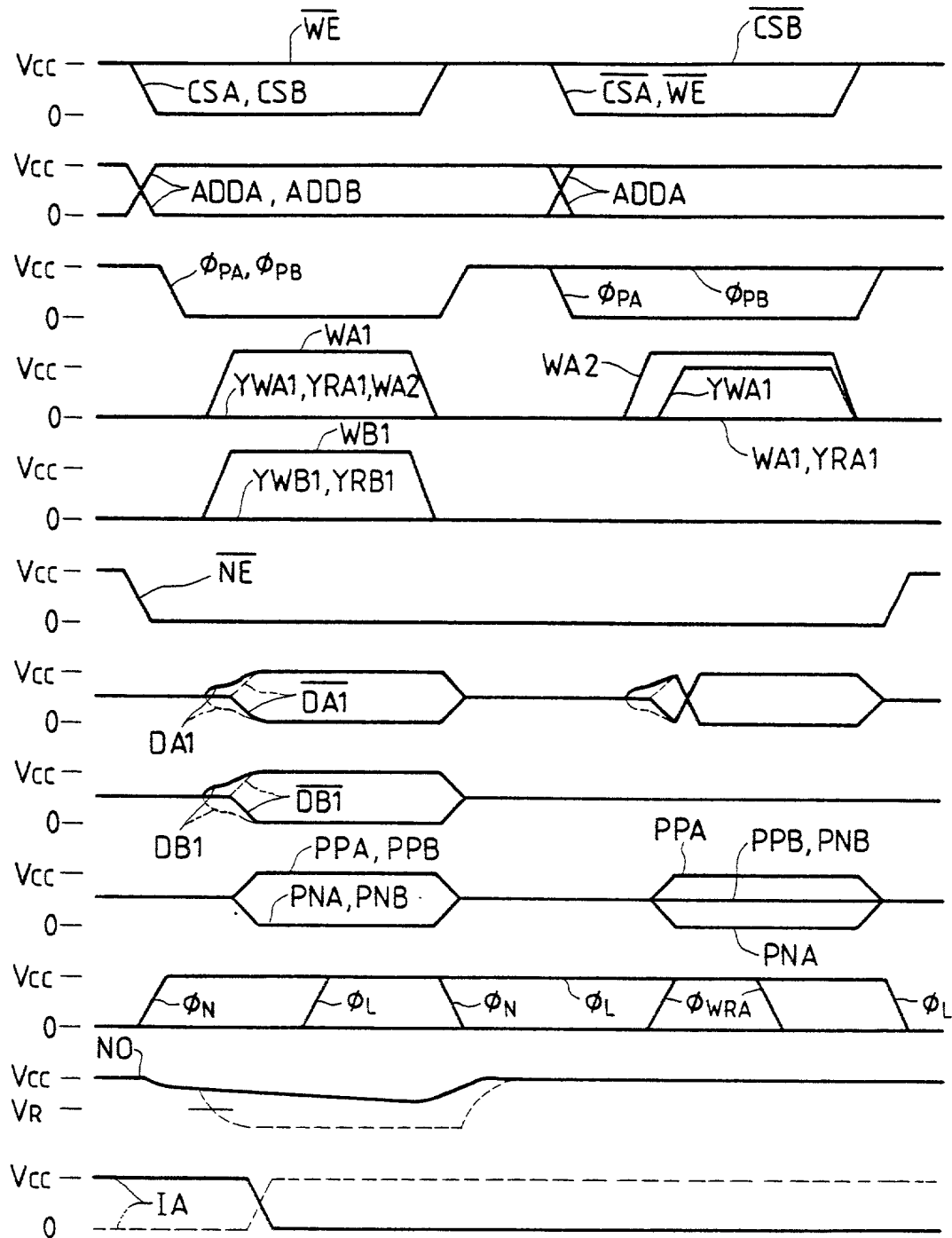


FIG. 14(a)

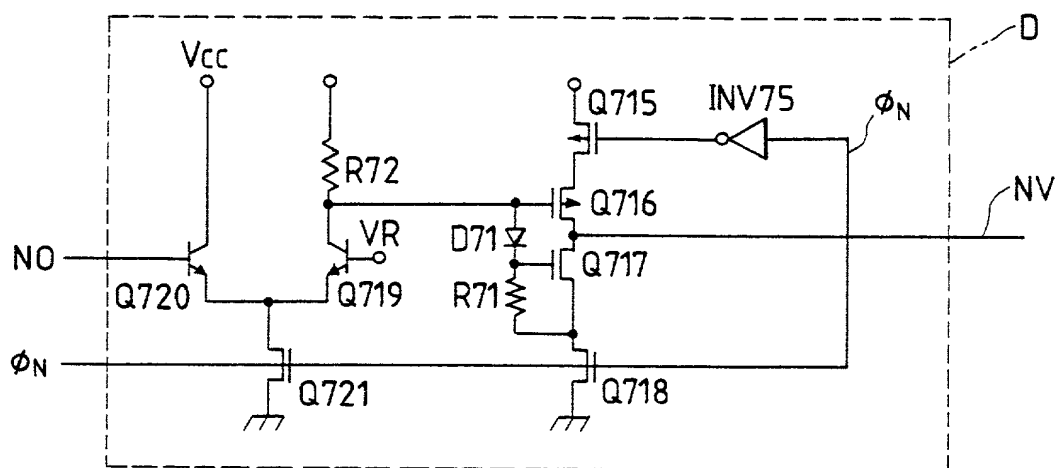


FIG. 14(b)

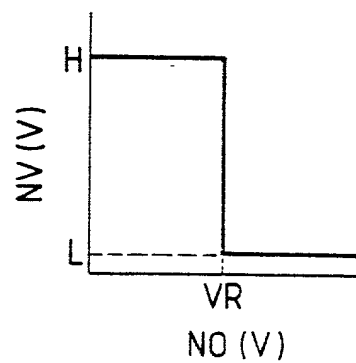


FIG. 14(c)

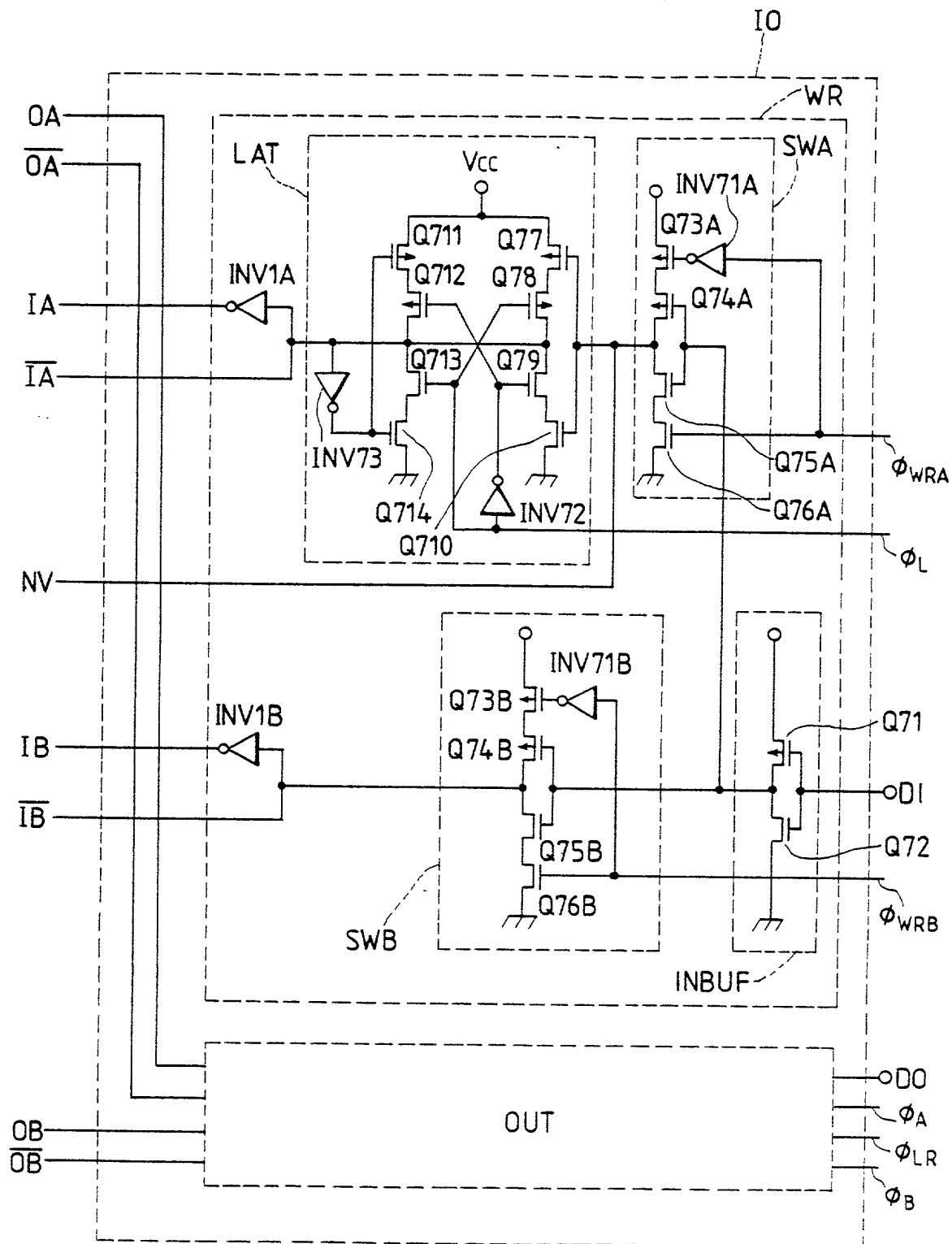


FIG. 14(d)

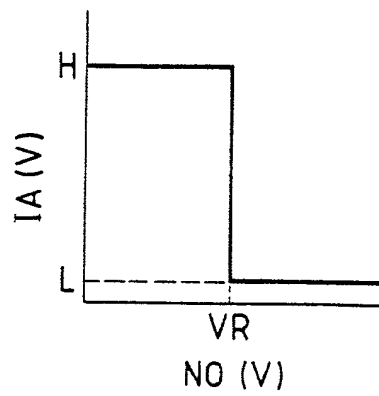


FIG. 14(e)

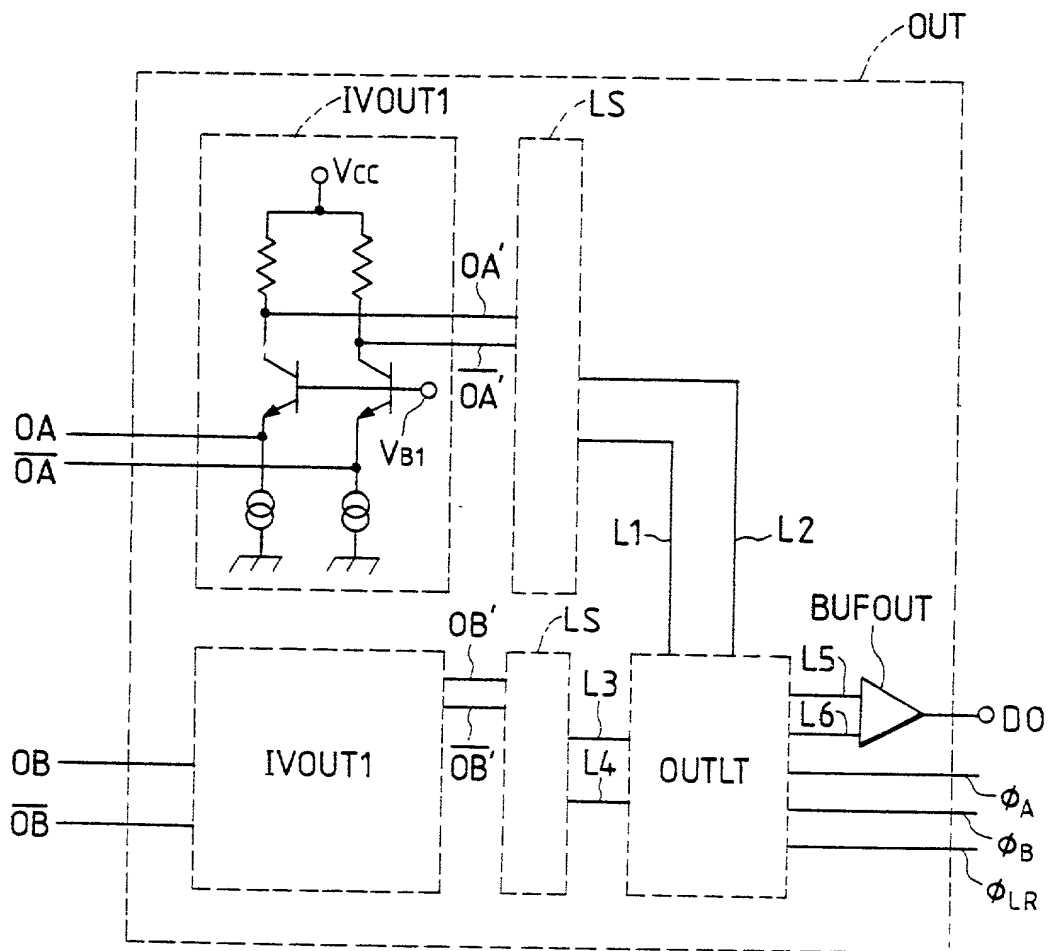


FIG. 14(f)

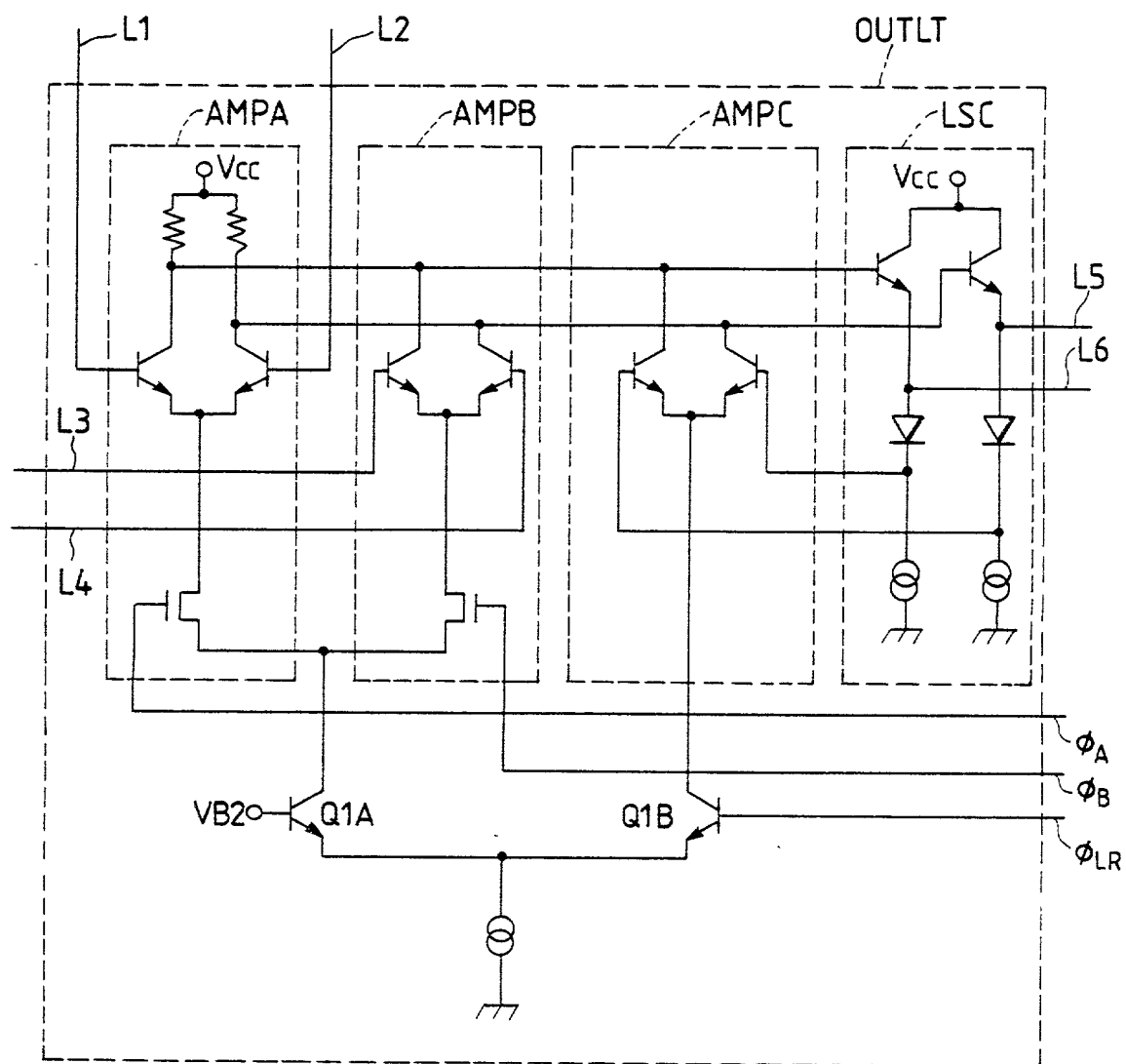




FIG. 15

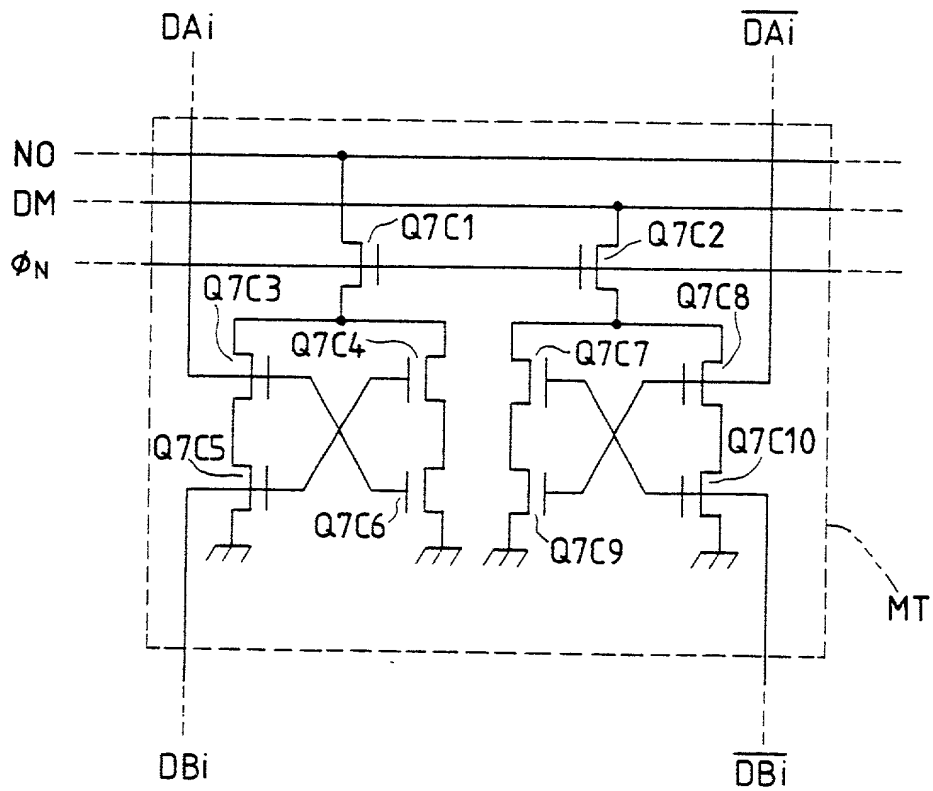


FIG. 16(a)

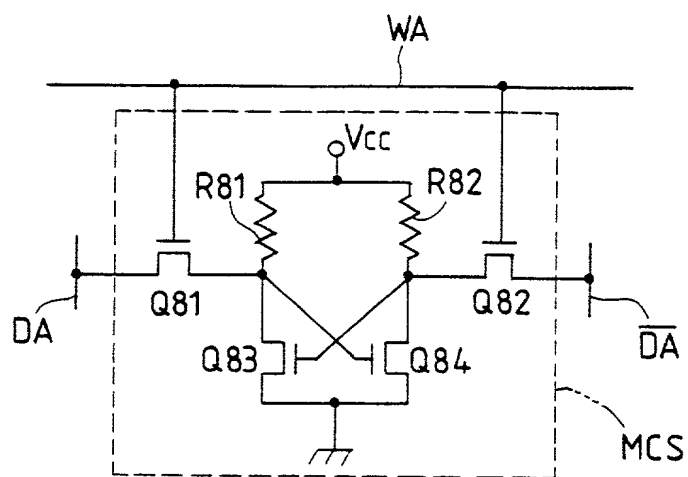


FIG. 16(b)

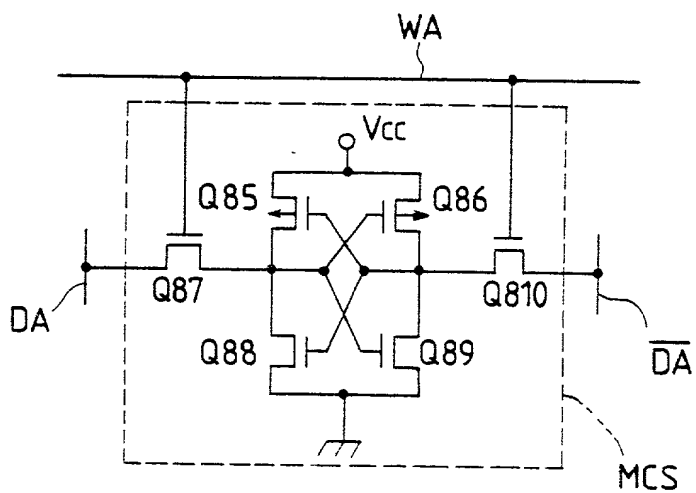


FIG. 16(c)

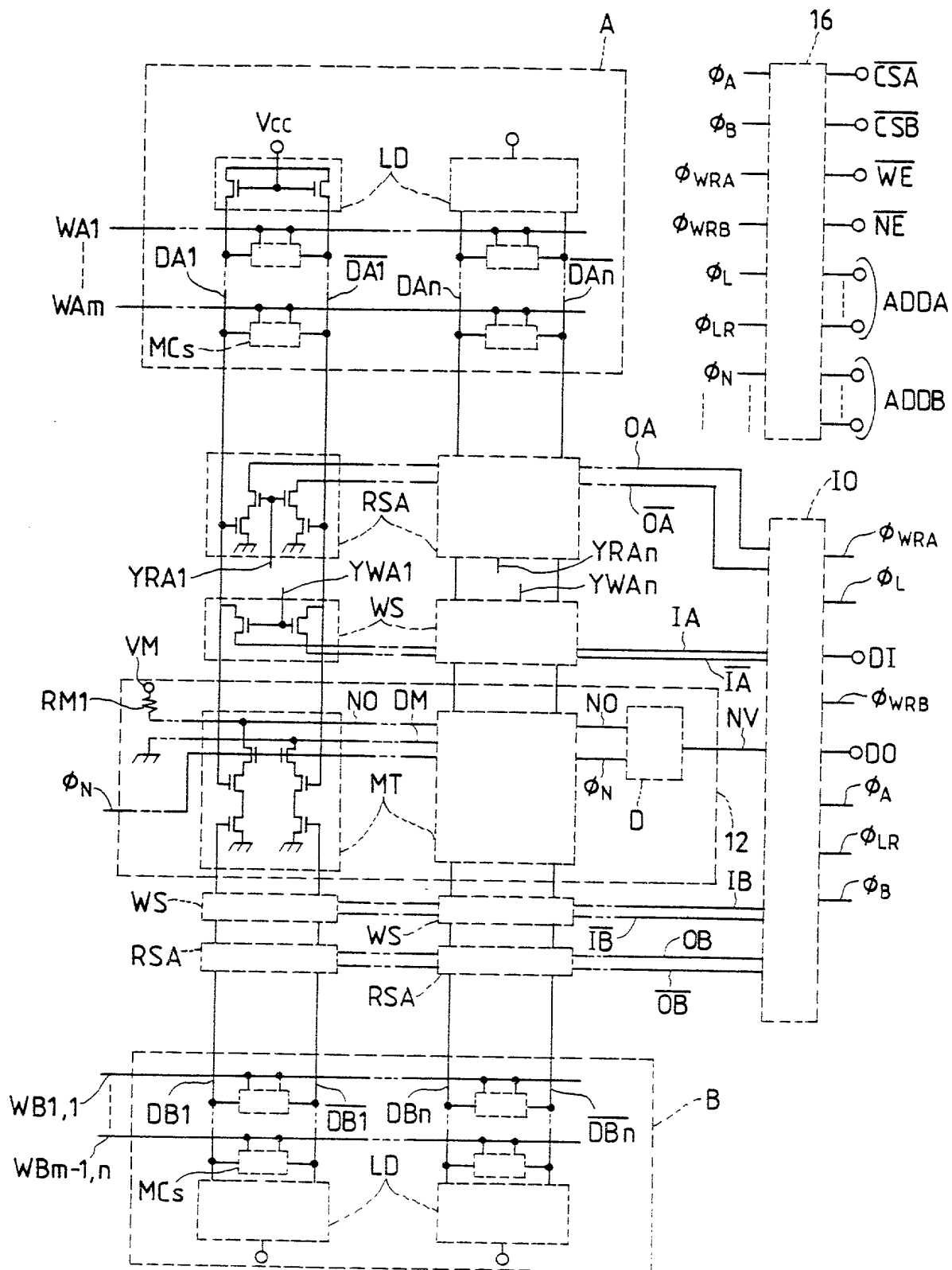


FIG. 17(a)

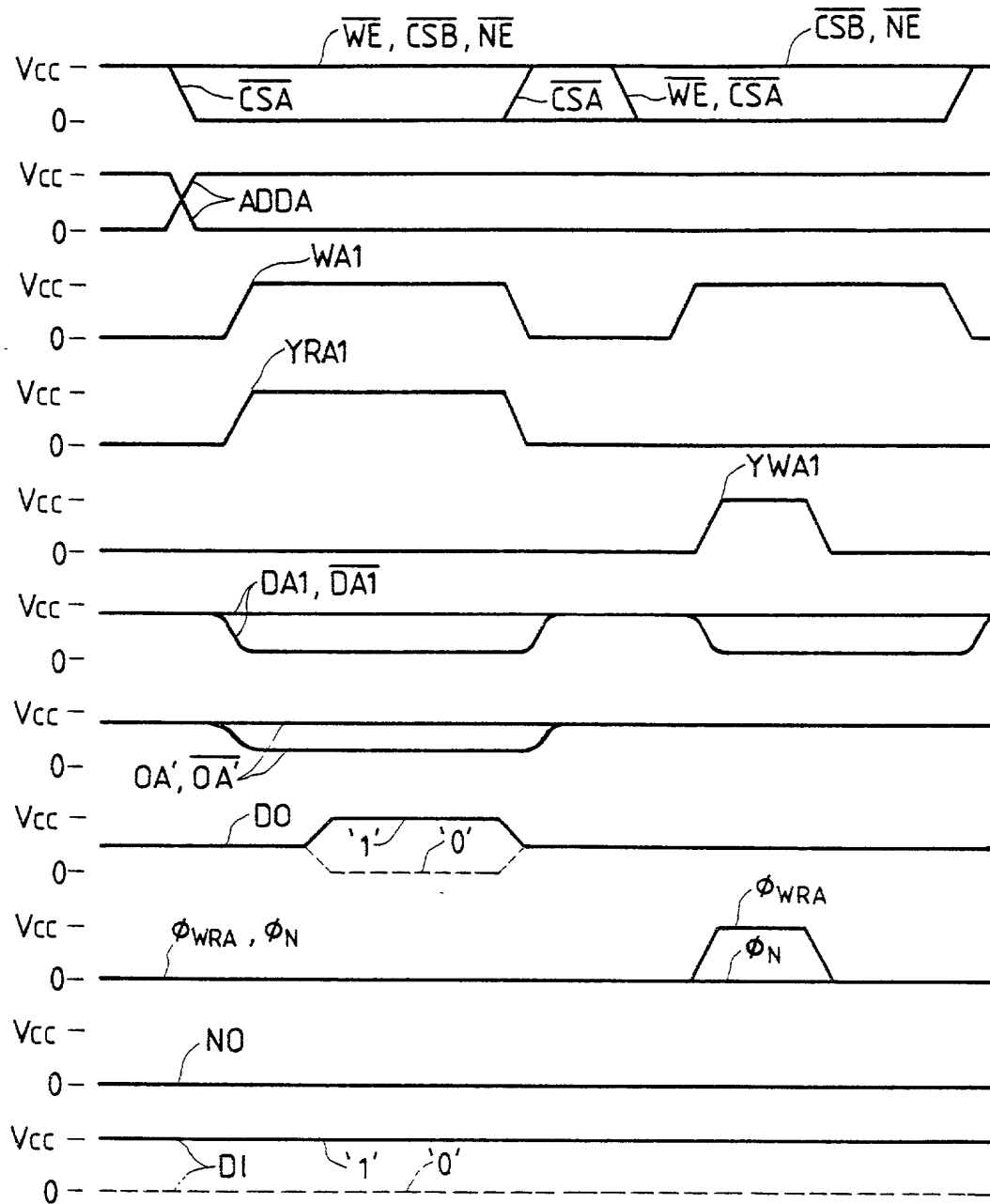


FIG. 17(b)

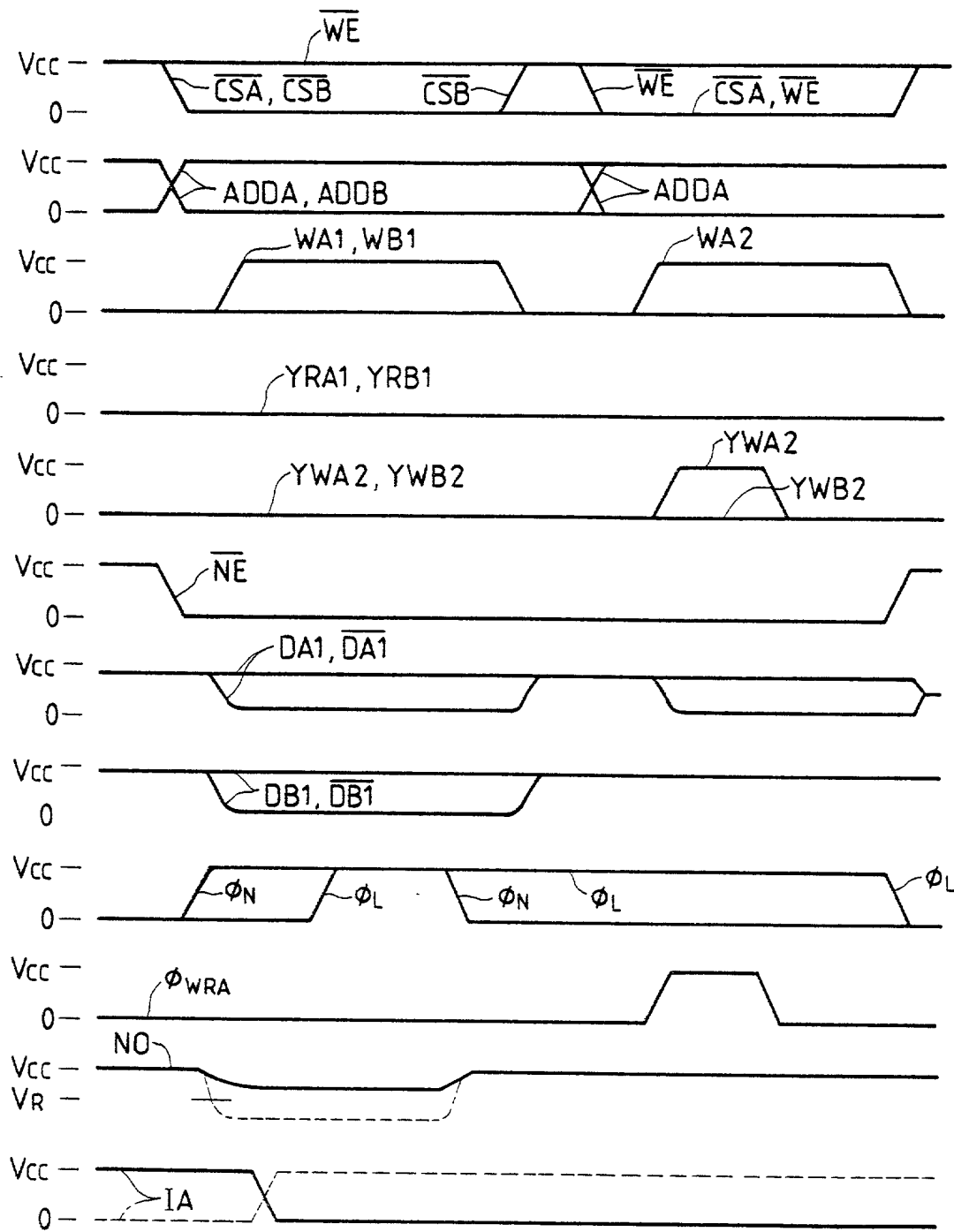


FIG. 18(a)

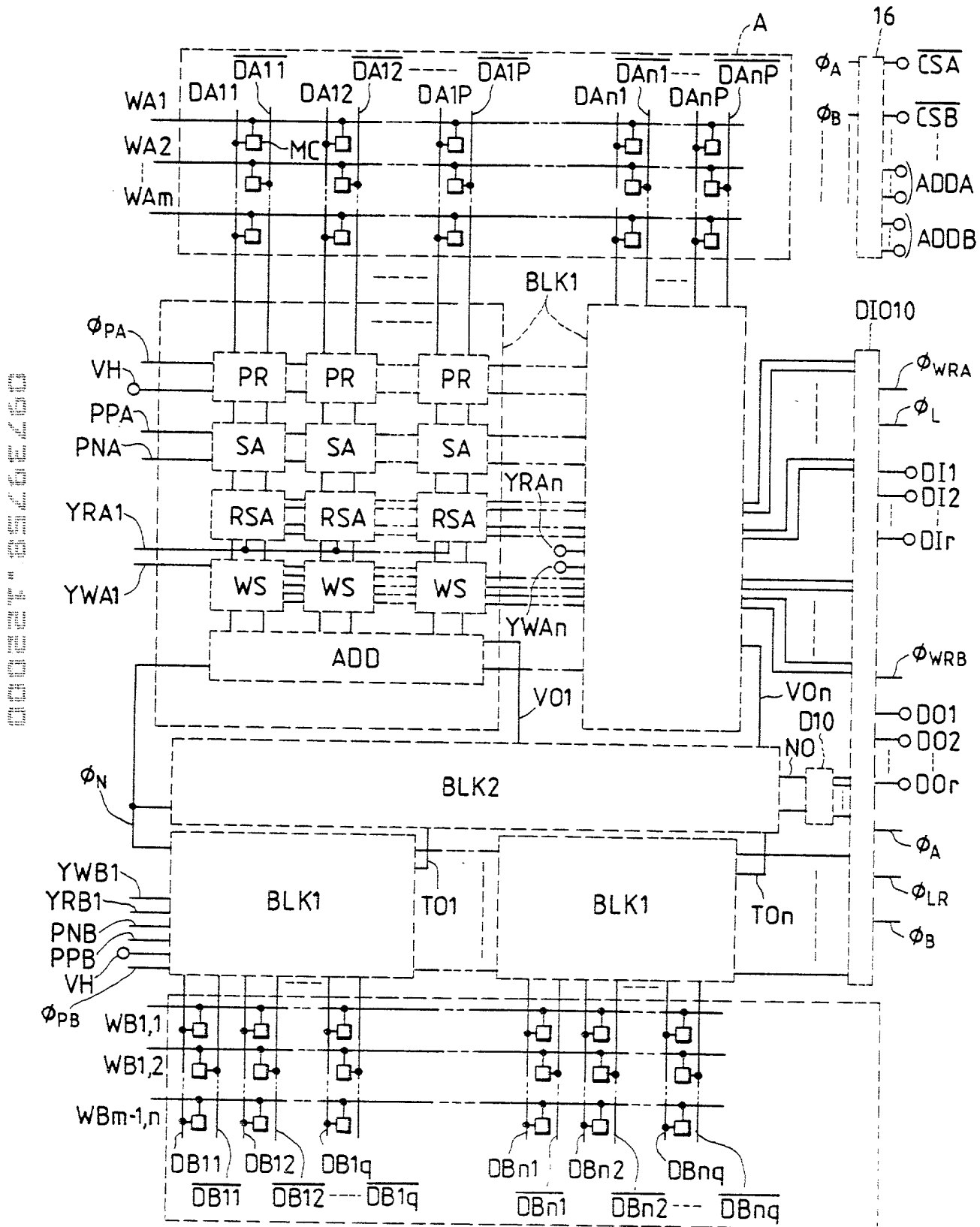
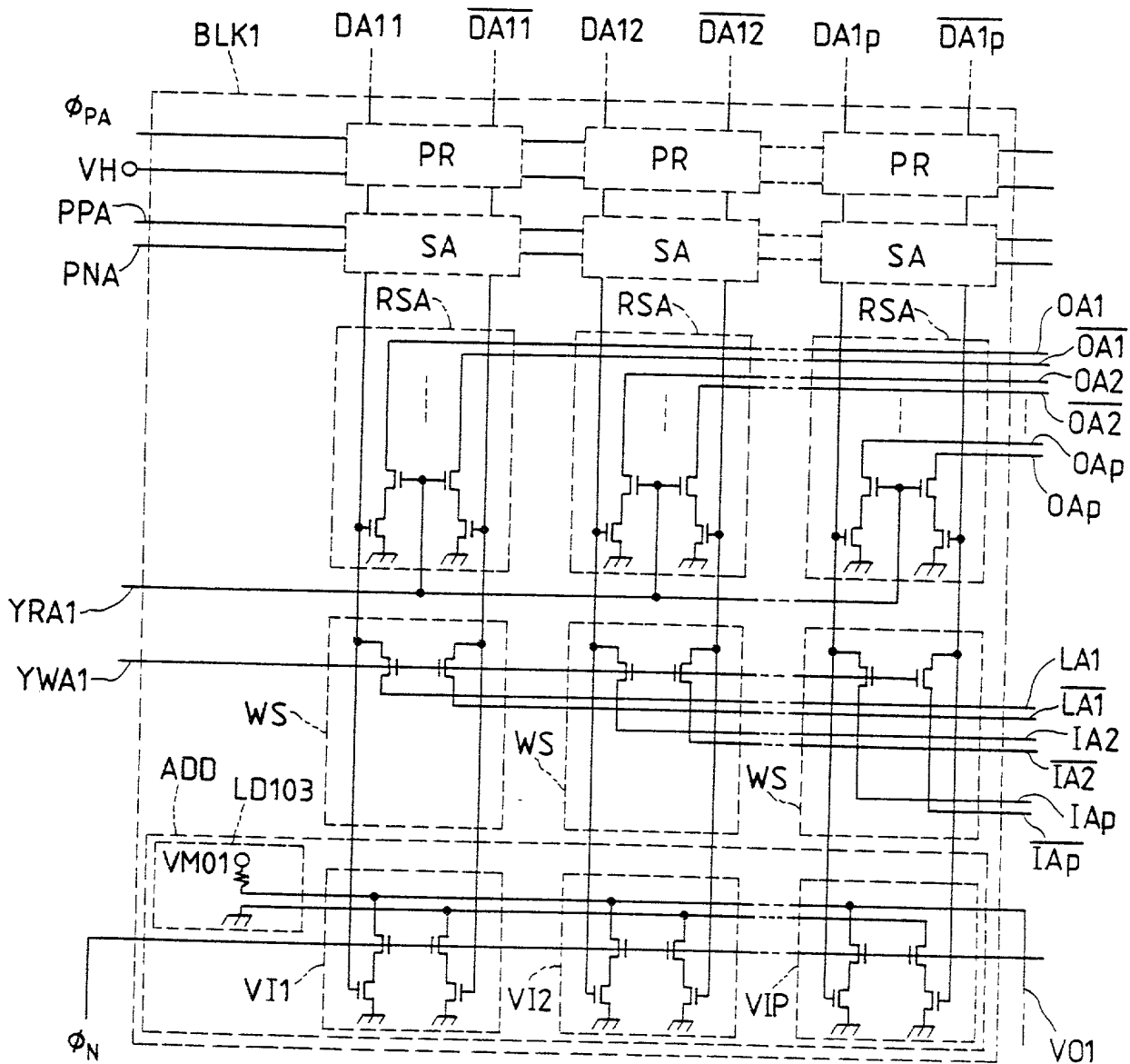


FIG. 18(b)



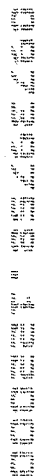
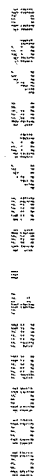
[illegible][illegible]



FIG. 18(e)

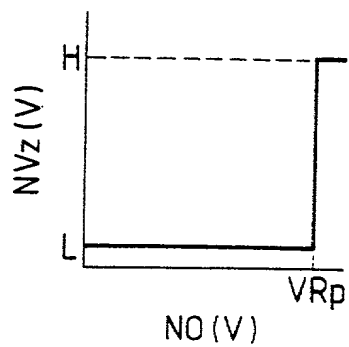
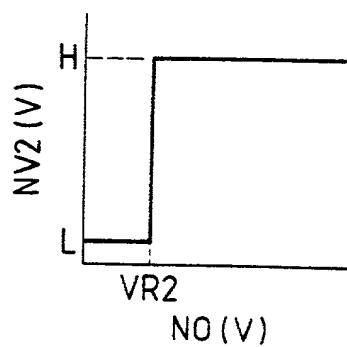
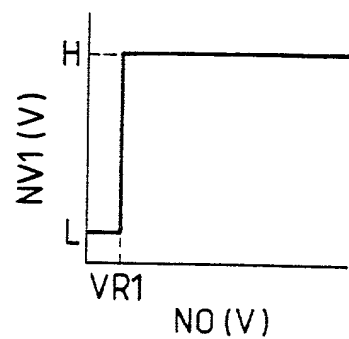


FIG. 19(a)

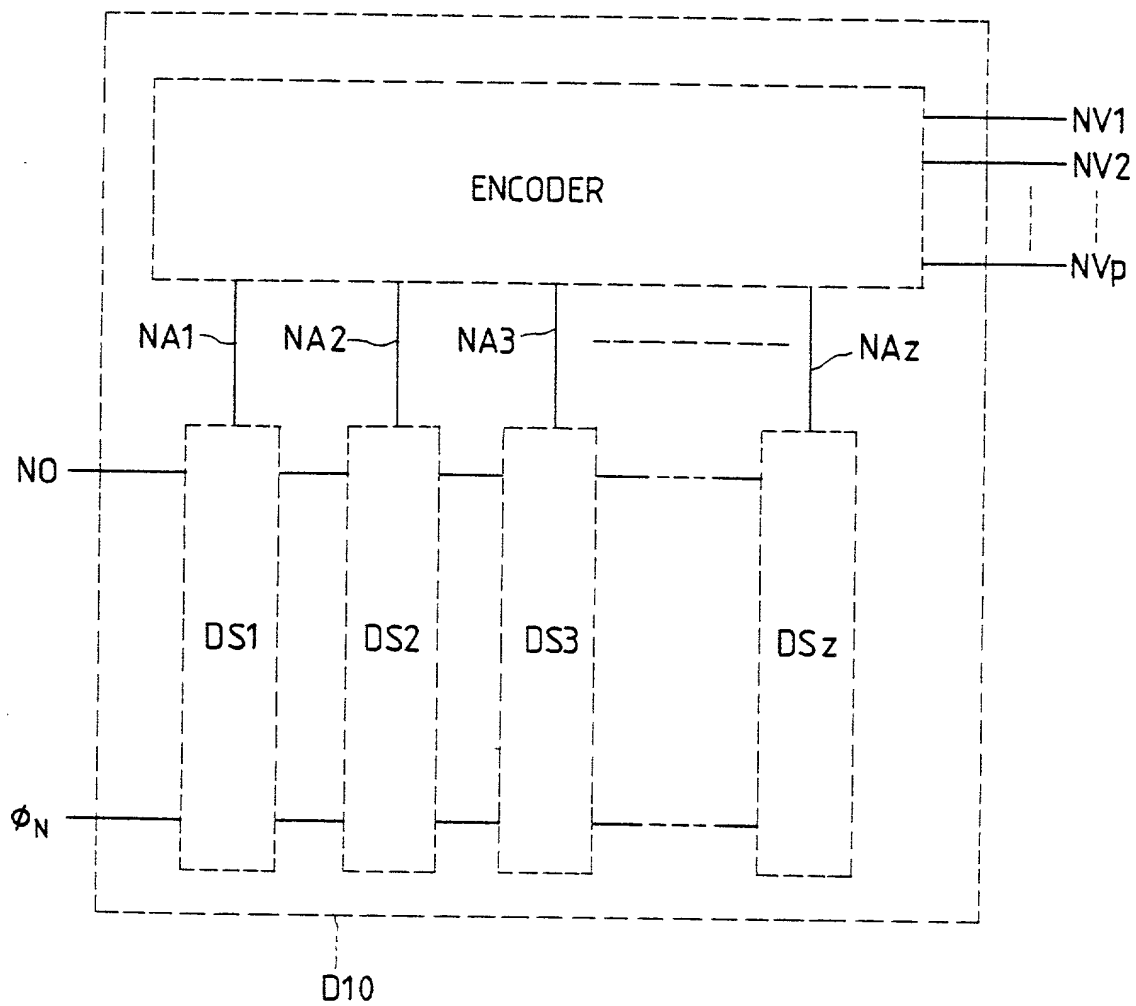


FIG. 19(b)

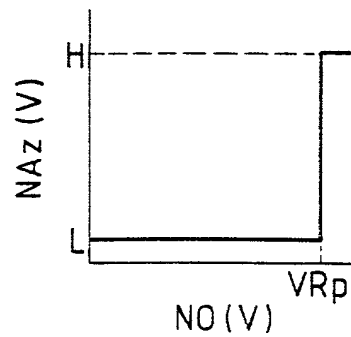
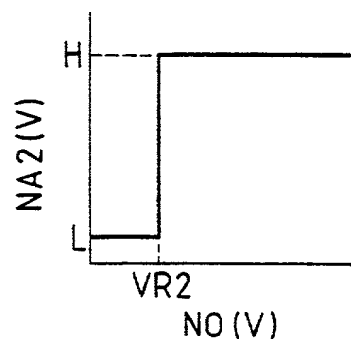
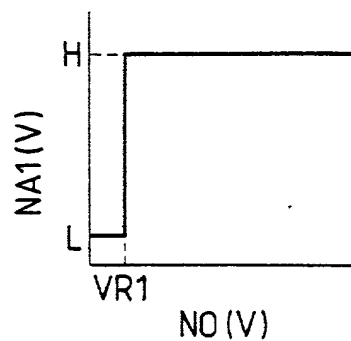


FIG. 19(c)

ENCODER INPUT						ENCODER OUTPUT					
NA1	NA2	NA3	NA4	---	NAz	NV1	NV2	NV3	NV4	---	NVp
L	L	L	L	---	L	L	L	L	L	---	L
H	L	L	L	---	L	H	L	L	L	---	L
H	H	L	L	---	L	L	H	L	L	---	L
H	H	H	L	---	L	H	H	L	L	---	L
H	H	H	H	---	L	L	L	H	L	---	L
⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮
H	H	H	H	---	H	H	H	H	H	---	H

FIG. 19(d)

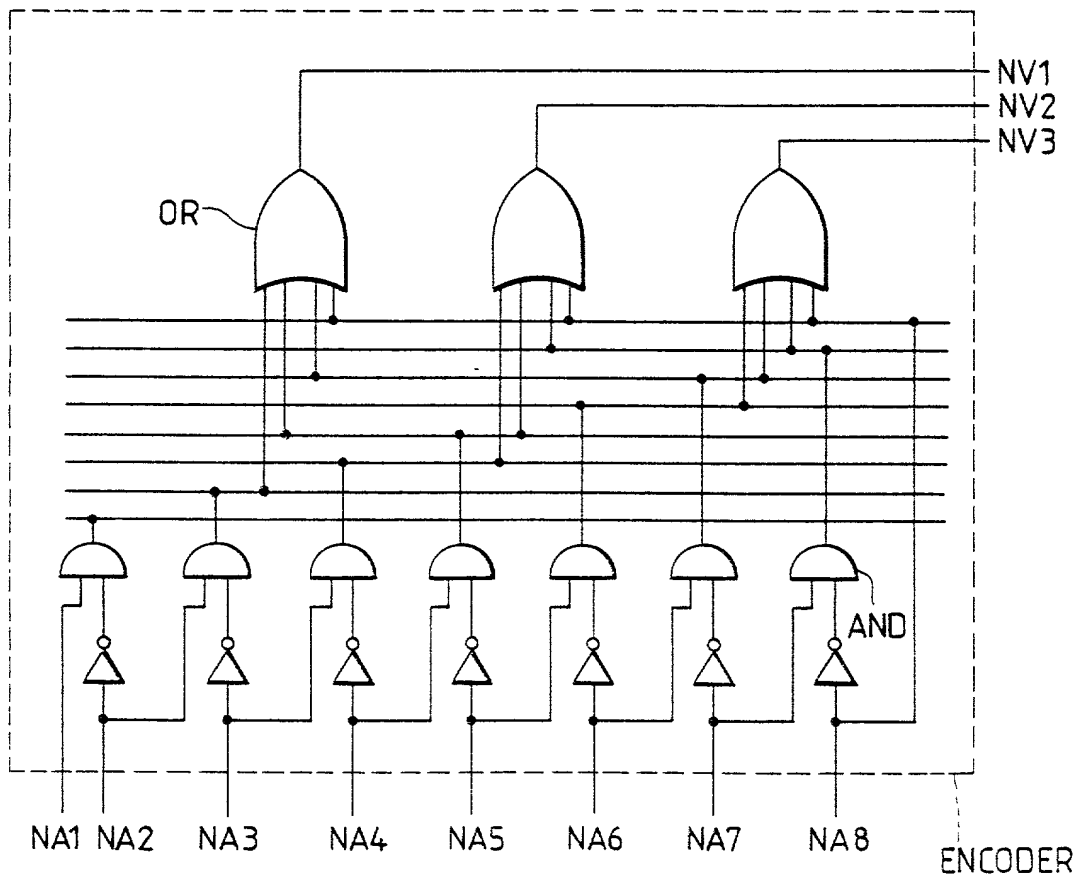


FIG. 20(a)

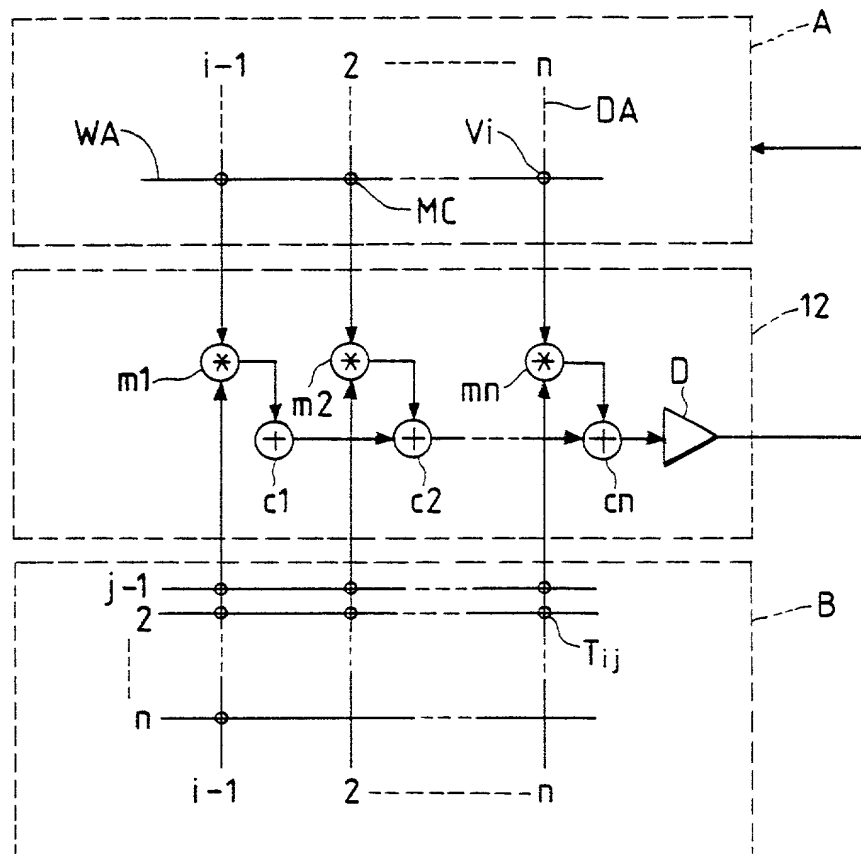


FIG. 20(b)

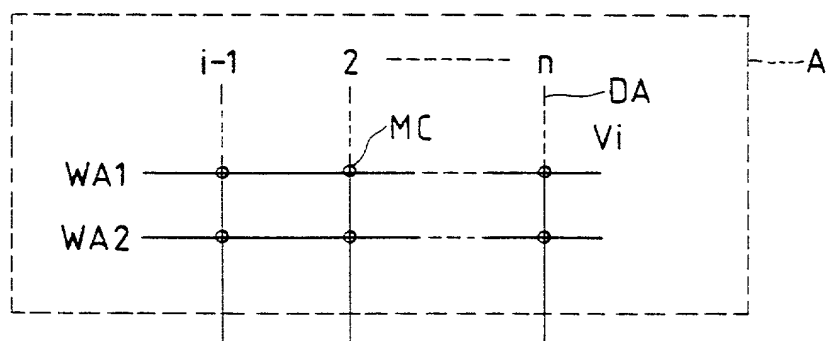


FIG. 21(a)

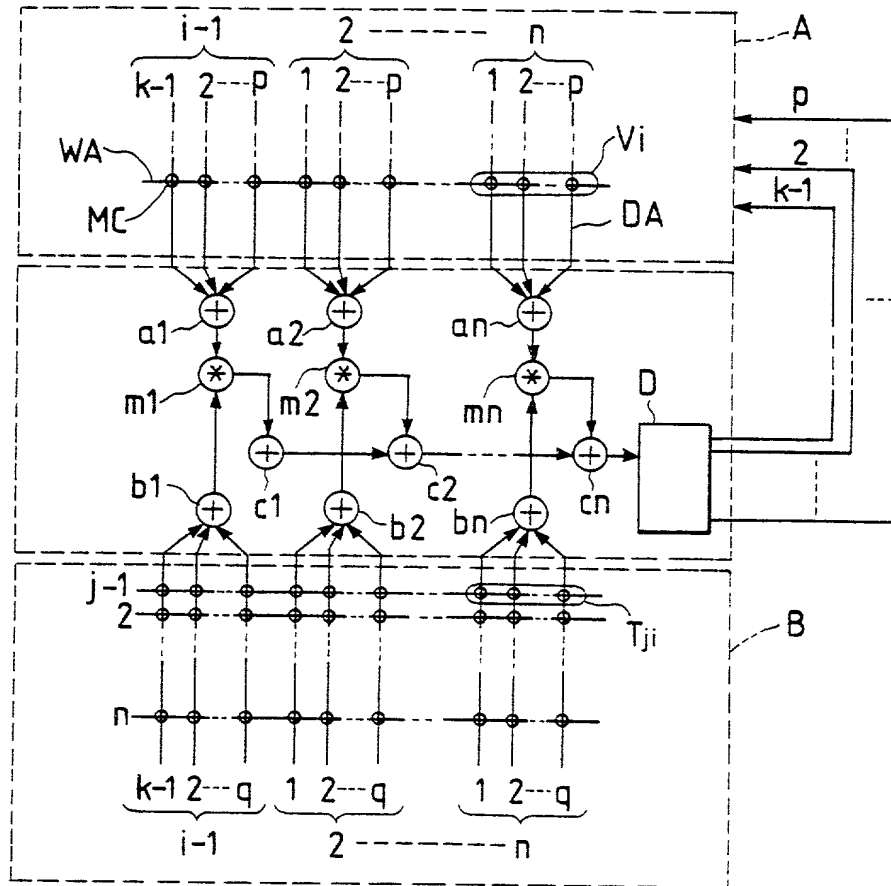


FIG. 21(b)

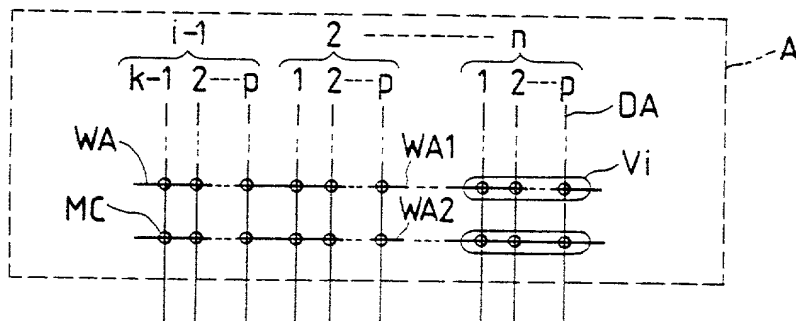


FIG. 22

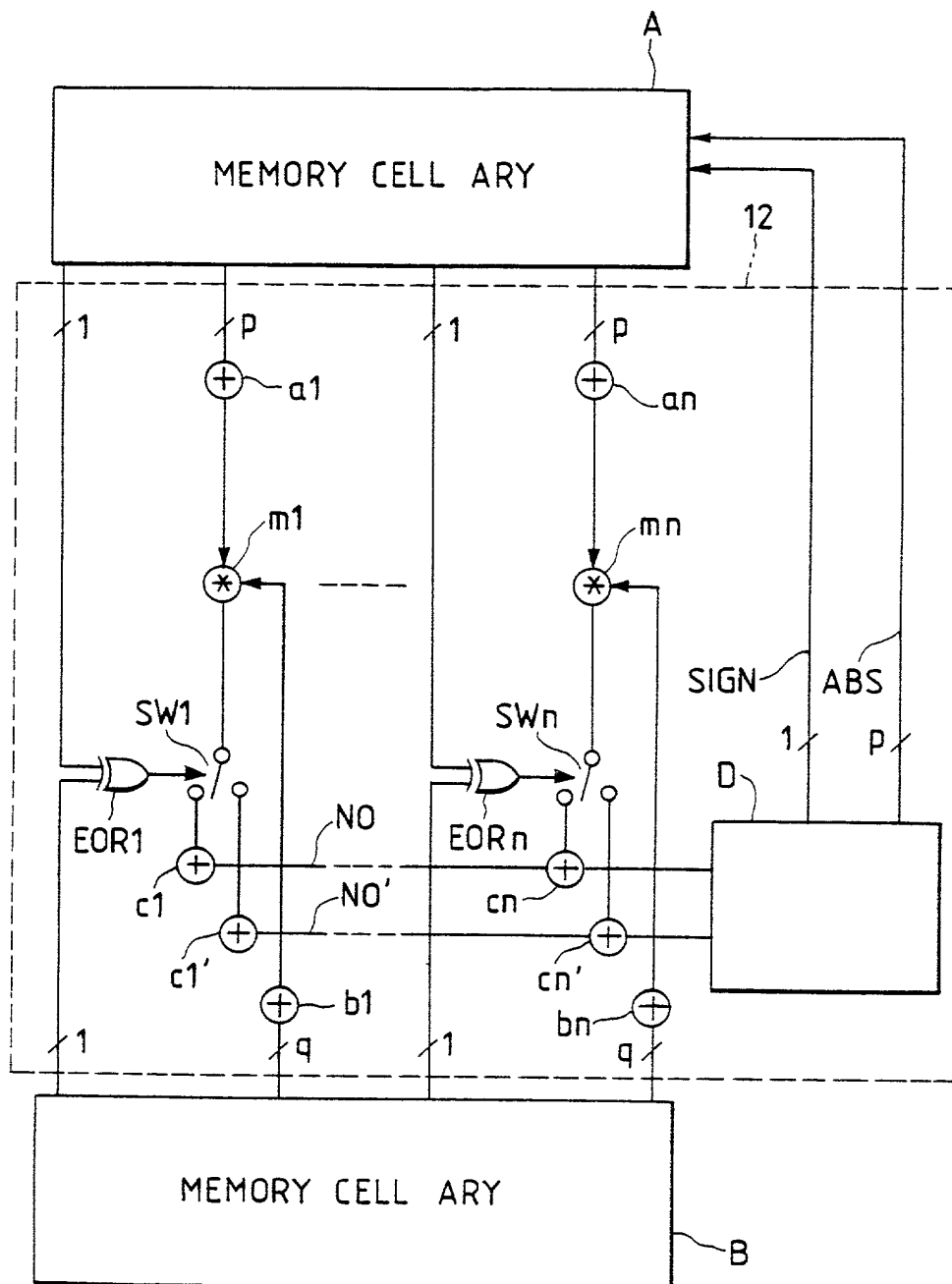


FIG. 23

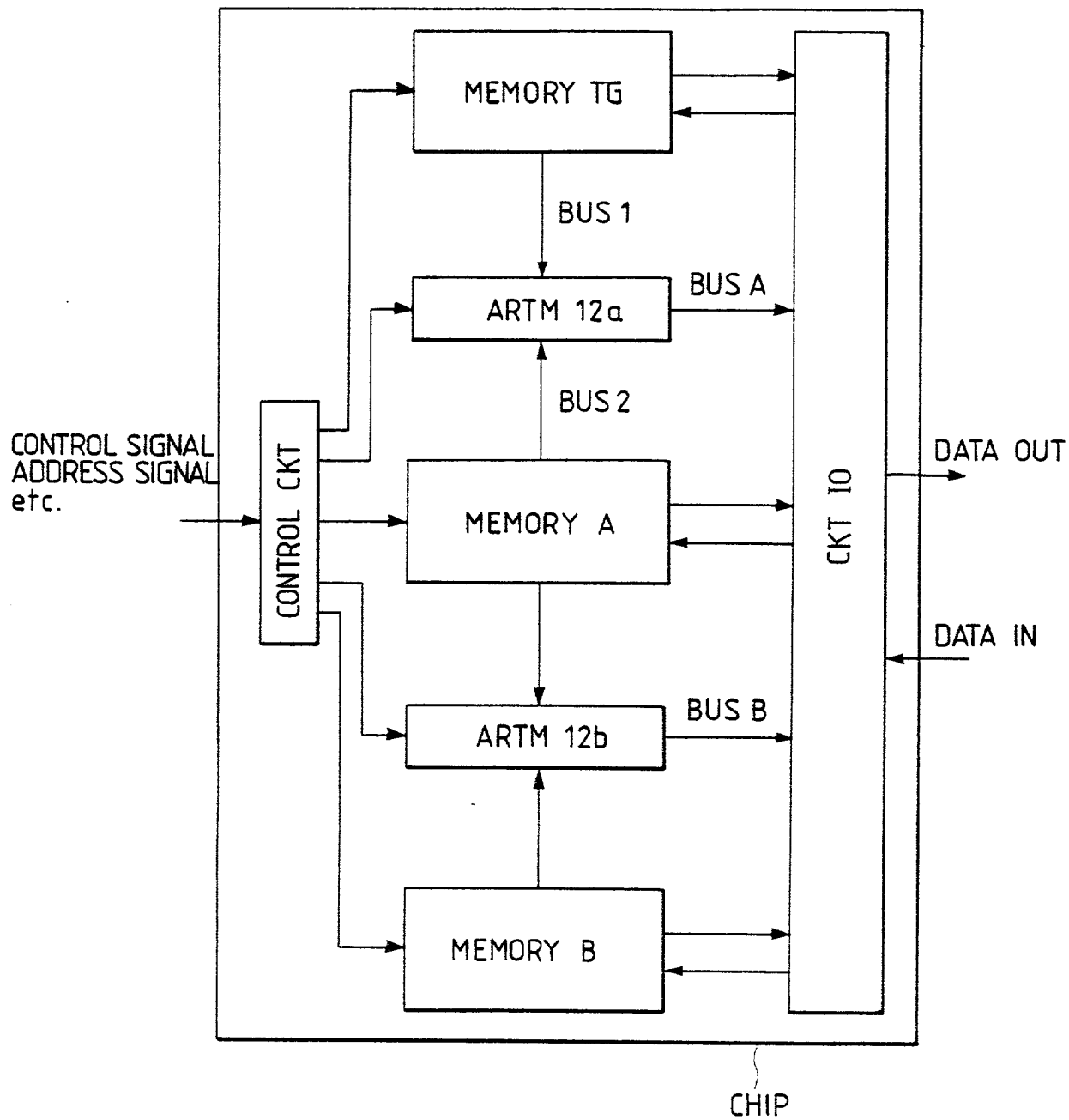




FIG. 24

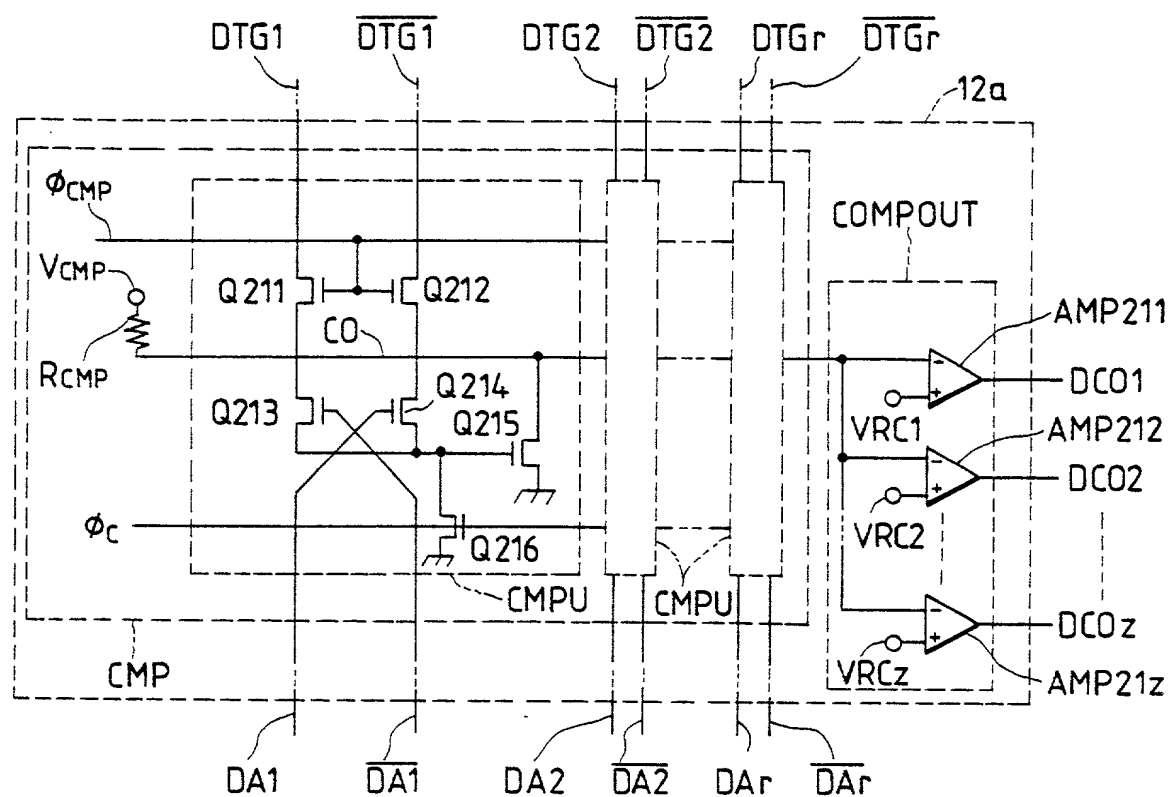


FIG. 25

